

xSAP The eXtended Safety Assessment Platform Version 1.4

xSAP User Manual



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This document is part of the distribution package of the XSAP toolset.

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Chapter 1

Introduction

The design of complex systems requires the ability to analyze, in additional to functional correctness, also the way faults are dealt with. This approach, known as safety assessment, relies on the definition of possible faults of a system, and results in the construction of important artifacts such as Fault Trees and FMEA tables.

Purpose of this document is to illustrate the usage and underlying principles of xSAP (eXtended Safety Assessment Platform). xSAP is a tool for the formal analysis and safety assessment of (complex) systems. xSAP supports a formal approach, based on principles of symbolic model checking. With respect to a model checker, xSAP provides the following functionality:

- Fault Extension: starting from a nominal model, describing the behaviour of a system without faults, an extended model is produced, that also contains behaviours in the presence of faults. Such extended model can be modeled manually (manual fault extension – compare Section 3.1) or automatically, starting from a library of faults and a definition of the faults to be injected into the nominal model (automated fault extension – compare Section 3.2).
- **Fault Tree Analysis:** given an extended model, and a specific property, xSAP produces the fault trees collecting all the minimal cut sets that can result in a property violation. This technique is classified as a *top-down* analysis compare Section 4.2.
- **FMEA tables:** given an extended model, xSAP produces FMEA tables, describing which properties are violated as a consequence of the occurrence of which faults. This technique is often referred to as a *bottom-up* technique, proceeding from the faults to the top properties compare Section 4.3.

At the core, xSAP relies on the NUXMV model checker [15], from which it inherits the modeling language. Within xSAP, the functional verification capabilities of NUXMV are extended to deal with model extension and safety assessment.

xSAP is designed to provide the following advantages. First, xSAP supports a methodology that allows for a tight integration between the design and the safety teams. Second, it automates (some of) the activities related both to the verification and to the safety analysis of systems in a uniform environment. Third, the use of the platform is compatible with an incremental development approach, based on iterative releases of the system model at different levels of detail. **History of xSAP** xSAP is developed and maintained by the Embedded Systems Unit of FBK. xSAP is a reimplementation of the FSAP/NUSMV-SA platform. FSAP/NUSMV-SA (Formal Safety Analysis Platform) was developed since 2001, with the support of the European Union, within several projects in the areas of formal verification and safety analysis: the ESACS project, the ISAAC Project, and the MISSA Project. The techniques underlying FSAP/NUSMV-SA have been subsequently applied in several projects funded by the European Space Agency: COMPASS¹, AUTOGEF², FAME³, and HASDEL⁴.

Structure of the document This manual is organized as follows:

- Chapter 2 describes the overall process supported by xSAP.
- Chapter 3 explains the Fault Extension methodology.
- Chapter 4 presents the available forms of safety analysis.
- Chapter 6 gives and overview of the Fault Detection and Isolation analysis.
- Chapter 7 shows a complete example of use.
- Chapter 8 draws some conclusions and outlines the directions of future development.

The appendix contains the following information:

- Chapter A specifies the necessary hardware/ software configuration needed to run the xSAP toolset and the required installation steps.
- Chapter B describes the Syntax Directed Editor for the fault extension instruction language.
- Chapter C describes the available scripts provided by xSAP.
- Chapter D describes the available commands.

This document is not self contained, and assumes that the reader is familiar with NUXMV. The interested reader is referred to [20]. A technical description of the engines underlying NUXMV and xSAP can be found in [16, 6].

¹http://www.compass-toolset.org

²http://autogef-project.fbk.eu

³http://fame-project.fbk.eu

⁴http://hasdel-project.fbk.eu

Chapter 2

Methodological Overview

2.1 A Formal Approach to System Design

We consider a unified process that covers the modeling and the verification of complex and safety critical systems. The process, depicted in Figure 2.1, aims to support the early design phases by developing systems at an architecture level.

- **Requirements Validation:** In order to ensure the quality of requirements, they can be validated independently of the system. This includes both property consistency (i.e., checking that requirements do not exclude each other), property assertion (i.e., checking whether an assertion is a logical consequence of the requirements), and property possibility (i.e., checking whether a possibility is logically compatible with the requirements). Altogether these features allow the designer to explore the strictness and adequacy of the requirements. Expected benefits of this approach include traceability of the requirements and easier sharing between different actors involved in system design and safety assessment. Furthermore, high-quality requirements facilitate incremental system development and assessment, reuse and design change, and they can be useful for product certification.
- **Functional verification:** Analyzing operational correctness is the first step to be performed during the system development lifecycle. It consists in verifying that the system will operate correctly with respect to a set of functional requirements, under the hypothesis of nominal conditions, that is, when software and hardware components are assumed to be fault-free. One particular instance of this general model-checking problem that is specifically supported by the toolset is deadlock checking, i.e., ensuring that the system does not give rise to terminating computations. This is usually required for reactive systems. Moreover the toolset offers the feature to interactively simulate the execution of the system.
- **Fault Extension:** In general the behavior of a critical system is modeled in two parts: nominal and faulty. The former describes the system when it is not affected by any faulty behavior, while the second extends the nominal one with the possibility to have some undesirable behavior (e.g. a battery does not provides electricity, a link becomes broken, etc). The Model Extension permits to keeps disjoint the nominal and the faulty behavior of the system such that is possible to have a greater verification coverage. Fault

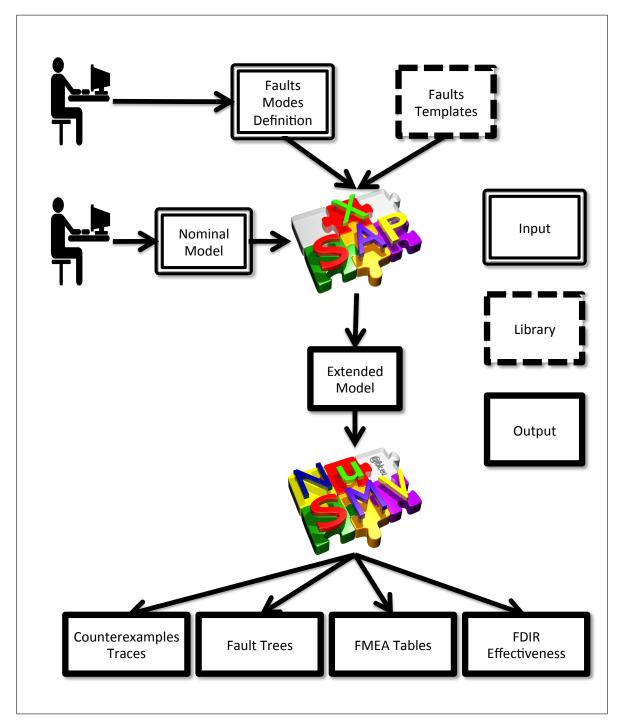


Figure 2.1: Overview of the methodology

extension can be carried out manually (compare Section 3.1) or automatically (compare Section 3.2)¹.

Safety and Dependability Analysis [22, 21, 24, 23]: Analyzing system safety and de-

 $^{^{1}}$ Figure 2.1 depicts the automated fault extension mechanism, which takes as input fault templates taken from a library, and set of fault extension instructions that define how the faults are to be injected into the nominal model

pendability is a fundamental step that is performed in parallel with system design and verification of functional correctness. The goal is to investigate the behavior of a system in degraded conditions (that is, when some parts of the system are not working properly, due to malfunctions) and to ensure that the system meets the safety requirements that are required for its deployment and use. Key techniques in this area are (dynamic) fault tree analysis, (dynamic) Failure Modes and Effects Analysis (FMEA), fault tolerance evaluation, and criticality analysis.

Fault Detection and Identification Analysis: Fault tolerant systems often implement some mechanisms to detect, identify and recover from, faults – i.e. an FDIR (Fault Detection, Identification and Recovery) sub-system. Fault Detection and Identification (FDI) is carried out by dedicated modules, called FDI components, running in parallel with the system. The detection task is the problem of understanding whether a component has failed, whereas the identification task aims to understand exactly which fault occurred. Typically, faults are not directly observable and their occurrence can only be inferred by observing the effects that they have on the observable parts of the system. FDI components take as input sequences of observations (made available by sensors) and trigger a set of alarms in response to the occurrence of faults.

2.2 Tool support with nuXmv and xSAP

The process is supported by two tools: NUXMV and xSAP. NUXMV mainly targets the phases of requirements analysis, design and verification. xSAP supports the phases of fault extension and safety assessment.

The steps of the methodology covered by functionalities of the NUXMV model checker are "traditional" formal verification steps. NUXMV is an extension of the NUSMV model checker along two key directions: it has a much stronger engine to deal with finite state models, and allows to deal with infinite state transition systems. The NUXMV model checker [15], that can be downloaded at the NUXMV webpage (http://nuxmv.fbk.eu/) More details at [15]. In this section we only present a overview of the NUXMV, and refer the reader to the NUXMV User Manual [20] for details.

xSAP builds upon NUXMV in the following way. In xSAP the nominal models, as well as the models resulting from fault extension, can be expressed in the language of NUXMV, which is, modulo minor variations, the language of NUSMV.

The properties used to represented the behavior of the system, be it nominal or extended, are written in form of temporal properties (either as invariants, LTL, or CTL) in the NUXMV language.

NUXMV provides several functionalities, that are also available in xSAP. These include functional verification, simulation, deadlock checking.

Chapter 3

Fault Extension

Given a model describing the nominal behaviour of a system, *fault extension* is a process that, based on a specification of the possible faults, returns a model whose behaviour takes into account also faulty behaviors. xSAP allows the user to handle the fault extension phase manually or automatically.

Manual fault extension is carried out through the manual definition of a Fault Modes file (compare Section 3.1).

On the other hand, the automated fault extension (compare Section 3.2) relies on a set of Fault Extension Instructions (FEI), and a library of faults, defining fault effects and fault dynamics (compare Section 3.2.1). The FEI instructs how the faults should be injected into the nominal model. As a result of the extension, the nominal model is extended with faulty behaviors and a set of *fault variables* that enable such behaviors.

In a nutshell, in automated fault extension, the set of fault variables is automatically added to the nominal model by the tool, according to the FEI. In manual fault extension, instead, the user has to declare a set of existing variables in the (manually created) extended model, to be the set of fault variables. The set of fault variables is used by xSAP to carry out the subsequent analyses – compare Chapter 4.

Once the fault extension phase is complete, it is possible to list the set of fault variables using the shell command show_fault_variables, as shown in Appendix D.4.

3.1 Manual Fault Extension

Manual extension of a nominal model is possible through the execution of specific commands available in the tool's shell. The user needs to write a Fault Modes file (xml format) which must be specified when invoking the tool (see section D.1).

Once the Faults Mode file has been loaded properly, several analyses can be performed, for instance Fault Tree Analysis (see Section 4.2).

3.1.1 XML Format

The Fault Modes file used in manual fault extension must be compliant with the xsd provided in file data/schema/failure-modes.xsd.

The root element, named <compass>, consists of the following elements:

• <fmlist>: required list of faults modes

- <cclist>: optional list of common causes
- <obslist>: optional list of observables¹

Faults modes can be defined using the tag <fm> and are required to have the attributes name and nominal_value. Optionally, fault modes can be associated to a probability (attribute probability) and to a history variable (attribute history – see Section 4.1 for additional details). In case we want to define a latent fault mode (compare Section 4.2.1), we need to add a child element named <latent> containing the related probability as an attribute.

If common causes are present (compare Section 4.5), they are introduced by element <cc> and must have an attribute name representing the name and an attribute fm representing the fault mode they refer to; in addition, it is possible to specify a proability through the attribute probability. Each common cause is associated to at least one mode (identified by the child element <cc_mode>) which requires to have specified a name and an interval (represented through attributes low and high) within the failure will be raised.

An example of valid Fault Modes file is depicted in figure 3.1.

```
<?xml version = "1.0"?>
  <compass>
2
    <fmlist>
3
       <fm name="SC.G3.Gen_StuckOff.mode_is_stuckAt_Off"</pre>
4
            nominal_value="FALSE" probability="0"></fm>
       <fm name="SC.G2.Gen_StuckOff.mode_is_stuckAt_Off"</pre>
           nominal_value="FALSE" probability="0"></fm>
       <fm name="SC.G1.Gen_StuckOff.mode_is_stuckAt_Off"</pre>
8
           nominal_value="FALSE" probability="0"></fm>
9
     </fmlist>
    < cclist >
11
     </cclist>
    <obslist>
13
     </obslist>
14
  </compass>
```

Figure 3.1: Example of Fault Modes file

3.2 Automated Fault Extension

The automatic extension of a nominal model is possible through the execution of specific commands available in the tool's shell, or through a script which simplifies the whole process. The user needs to write a FEI file (human-readable textual and xml formats are both supported), and then the FEI file is processed when carrying out the extension.

The fault extension process enriches the model in two directions:

• it associates a faulty behaviour model with an instance of a nominal component implementation;

¹This tag is present for historical reasons, and not currently used

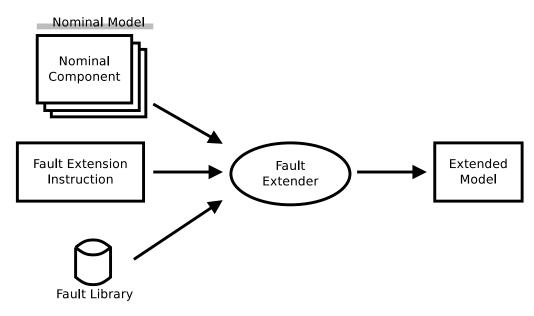


Figure 3.2: Automated Fault Extension flow

• it defines the dynamics controlling the occurrence of a faulty behavior in the respective component.

The process consists in extending a set of NCs (nominal components) and passes through the execution of the Fault Extender tool, which takes as inputs:

- The set of Nominal Components
- The Fault Library (see 3.6)
- The Fault Extension Instruction (FEI) containing a description of the way each NC should be extended (see section 3.4).

The Fault Extender produces the Extended Model as output, i.e. a SMV file containing the Nominal Model automatically extended with the faulty behavior.

The steps of the process are explained in greater details below.

3.2.1 Fault Extension Instructions

The language for fault extension is built on the following concepts.

A FE is a set of Nominal Components (NC) each associated with its set of Fault Slices.

Each Fault Slice contains the fault modes and their dynamics which affect a slice of the NC variables.

Formally, a FE is a set:

where:

NC a nominal component,

FS is a fault slice,

where a FS is composed of

AS is a non-overlapping subset of the symbols of NC which are affected by the fault

EM is an Effect Model

LDM a Local Dynamics Model

GDM is a Global Dynamics Model

3.2.2 Fault Slice

A Fault Slice (FS) is a set of Fault Modes with associated Global Dynamics Model and Affected Symbols (AS).

Formally:

$FS := \langle AS, \{\langle EM, LDM \rangle \}, GDM \rangle$

A FS applies its effects to a subset AS of the set of symbols of a NC. Intuitively, FSs allows for composing Fault Modes which may be thought as basic faults, to build more complex faults.

All Fault Slices of a NC are implicitly in cross-product, each operating independently on its slice over NC's variables.

3.2.3 Fault modes

One fault mode (fm) is made of:

- One Effects Model (EM)
- One Local Dynamics Model (LDM)

One fm has two modalities: nominal and fault.

Effect Model (EM)

Effects are constraints over a set of variables which are intended to be bounded to corresponding affected symbols AS (variable or defines) in the NC, whose values received through the FS interface. Each EM determines what are the effects of entering and during the fault mode.

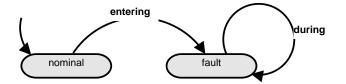


Figure 3.3: States and transitions of an EM

The effect can be function of the internal state of the EM. Predicates is_nominal and is_fault are available in EM to tell which is the current/next mode.

An EM applies its effects on an AS by writing to a corresponding output variable (OV). The EM can read the value of the AS through a corresponding input value. If two or more EMs write to the same AS, all the corresponding OVs must have the same type (which is type of the AS).

Local Dynamics Model (LDM)

LDM models how the fm moves between the nominal and fault modes. One or more fault events can be defined, and transitions between the modalities are defined. Like for EMs, predicates is_nominal and is_fault are available in EM to tell which is the current/next mode.

Interface of a fm

The fm interface results from the combination of the EM and the LDM.

The fm Interface is nominally made of:

- One or more input values, which carry the value of corresponding variables/defines in the NC for binding.
- One or more output variables which are constrained in the EM, each overwriting a corresponding input variable/define (in AS) in the NC passed through a corresponding input value.
- One or more additional read-only input parameters for constants, or other values coming from the NC.
- One or more template parameters which are resolved by string substitution.
- One or more events, defined and used in the EM and LDM.

Instantiation of a fm

One EM and one LDM are taken from the respective generic libraries, and associated. This requires that all template strings are resolved. The fm construction produces a fm which needs to be contextualized.

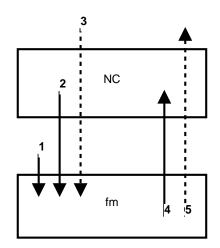


Figure 3.4: Binding NC's content into one fm

The fm interface can take values from constants (1), from the NC's internals (2), and from the NC's interface (3). The fm Interface can output (constrain) data and events to the internal state of NC (4) or to its interface (5).

All fms belonging to a FS are associated with one NC. This requires that:

- All input variables/defines which have to be bounded in the NC are associated with the corresponding output variables of the fm.
- All events of the fm which need to be constrained by the NC are connected to the corresponding values in the NC (NC's parameters and local symbols).
- All other input values are resolved, either attached to a constant value, or to values in the NC (NC's parameters and local symbols).
- All remaining template strings are resolved.

3.2.4 Global Dynamics Model

GDM defines the dynamics of composition of several fm's of each FS.

Two or more instantiated fms can be composed to define more complex behaviours.

- 1. A set of instantiated fms are chosen.
- 2. Transitions among them are defined.
- 3. New events may be added at this stage to be used as triggers in transitions.

In a FS's GDM the nominal modes of all instantiated fms are collected in one unique nominal mode, to compose a daisy-shaped automaton, whose leaves are the fail modes.

In Figure 3.5, three fms are composed, each has its LDM (in gray), but in addition also transitions between fail2 and fail1, and between fail2 and fail3 are defined (in blue). Those transitions can be labeled either with:

- Events which exist in the *target* fm, or
- *new* events created at this stage.

In the example **e1** is an event occurring in the **fm fail1**. **new_ev** is instead a newly created event.

3.3 Common Cause

A fault extension specification is made of one or more module's extensions, and optionally by one or more Common Causes (CCs).

Each CC represents a set of failures, possibly occurring in different components and different times, for which the assumption of fault independence does not hold. For example, failure of one power generator may happen standalone, or may happen as a consequence of a CC. In the latter case, the CC may specify that two generators may fail, and that the two failures are

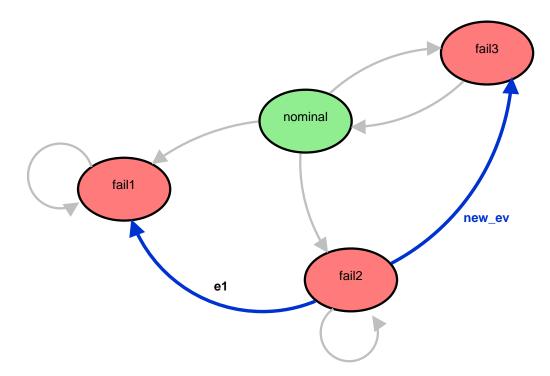


Figure 3.5: Global Dynamics Model among three fms

dependent, e.g., the first generators fails, and then the second generators fails between 1 and 3 discrete time-steps *as a consequence* of the common cause.

Each CC specifies an initiating event (the *common cause event*, which is distinct from the component fm themselves) and the set of the component fm with the corresponding timings (specified as intervals). Each interval specifies the time bounds (expressed relative to the time of the initiating event) at which the fm can take place.

3.4 The Fault Extension Instruction language

FEI is the way a user specifies how a Nominal Model has to be extended with faulty behaviours.

FEI can be specified either with a XML specification, or in a human-readable textual format.

The EBNF-like grammar of the FEI is presented below. A complete example of usage of FEI is presented in Section 7.3.2.

```
< dot > ::=
2
    <colon> ::= ':'
3
    <semi-colon> ::=
                        ';'
4
5
    <comma> ::=
    <lpar> ::=
6
                ')
7
    <rpar> ::=
8
    < lbra > ::=
9
    \langle rbra \rangle ::=
    < rw-op-lt > ::=
                     "<<"
    <rw-op-gt> ::= ">>'
11
12
    <eq-op> ::= '='
    <range-op> :== "...
13
   <trans-begin> ::= "-["
14
   <trans-end> ::= "]->"
15
```

1

```
16
17
    <line-comment> ::= -- .* \setminus n
    <multiline -comment> ::= /- [.\n]* --/
18
19
    <id> ::= [A-Za-z_][A-Za-z_0-9-]*
20
    \langle wildcard-id \rangle ::= [A-Za-z_* \setminus [\setminus]?] [A-Za-z_0 - 9* \setminus [\setminus]? - ]*
21
22
    <full-id> ::= <id> | <id> <dot> <full-id>
23
    <wildcard-full-id> ::= <wildcard-id> | <wildcard-id> <dot> <wildcard-full-id>
24
25
26
    < digits > ::= [0-9] +
    <number> ::= (+|-)? <digits>
27
28
    <float-number> ::= <number> <dot> (<digits>)?
29
    <exp-number> ::= <float-number> (e|E) <number>
30
    <real-number> ::= <float-number>
31
                      | <exp-number>
32
33
34
    # ·
    <start> ::= <fault-extension>
35
36
37
38
    # -
    <fault-extension>
39
        FAULT EXTENSION <id>
40
41
           (< mod - extension >) +
           (< \text{common-causes} >)?
42
43
44
    #
45
   # Fault slices and modes from here
46
47
   #
    <mod-extension> ::=
48
49
        EXTENSION OF MODULE < full - id >
          (< instances >)?
50
          <slice>+
51
52
    <slice> ::=
53
        SLICE <slice-id> (<instances>)? AFFECTS <var-list> WITH
54
           < fault - mode > +
55
           <global-dynamics>?
56
57
    <instances> ::=
58
        FOR INSTANCES <wildcard-full-id-list>
60
    <fault-mode> ::=
61
        MODE <mode-id> (probability-value-mode)? <colon>
62
              <local-dynamics-model-id> <effect> <semi-colon>
63
64
65
    <global-dynamics> ::=
66
        GLOBAL DYNAMICS
        <new-event>*
67
        < trans >+
68
69
70
    <new-event> ::=
71
                <event-par> <semi-colon>
72
73
    <slice -id> ::= <id>
    <mode-id> ::= <id>
74
75
76
    <local-dynamics-model-id> ::= <id>
77
78
    < effect > ::=
79
       < effect - id > < lpar > < par - list > < rpar >
80
81
    <effect -id> ::=
       <\!\mathrm{id}\!>
82
83
    < trans > ::=
84
    TRANS <trans-mode-id>
85
```

```
18
```

```
<trans-begin> <full_id >? <trans-guard>? <trans-end>
86
87
                  <trans-mode-id> <semi-colon>
88
     <trans-guard> ::=
89
              when < simple - expression >
90
91
92
     <trans-mode-id> ::=
                <mode_id>.nominal
93
                <mode_id >.fault
94
95
                nominal
96
     <var-list> ::=
97
98
                 < id >
                  | <id> <comma> <var-list>
99
100
     <par-list > ::=
101
                  < par >
103
                  | <par> <comma> <par-list >
104
     < par > ::=
106
           < data-par >
             < event - par >
108
            <template-par>
109
     <data-par> ::=
           data <id> <rw-op-expr>?
112
113
     <event-par> ::=
114
           event <id> <rw-op-expr>?
115
116
     <rw-op-expr> ::=
117
           <rw-op-lt> <simple-expression>
          | <\!\!\mathrm{rw-op-gt}\!> <\!\!\mathrm{id}\!>
118
119
     <template-par> ::=
120
            \texttt{template <id} > <\texttt{eq-op} > <\texttt{id} >
122
     <wildcard-full-id-list> ::=
123
           <\!{\rm wild} \, {\rm card} - {\rm full} \, - {\rm id} \! >
124
            | <wildcard-full-id> <comma> <wildcard-full-id-list>
125
126
127
128
     #
     # Common cause from here
130
     #
     <common-causes> ::=
131
132
         COMMON CAUSES
133
           (< \text{common-cause} >) *
134
135
     <common-cause> ::=
136
         CAUSE <id> (probability-value-cc)?
         (< cc-module-modes >)+
138
     <cc-module-modes> ::=
139
         MODULE <full-id>
140
            (< instances >)?
141
            (< cc - mode >) +
142
143
     <cc-mode> ::=
144
         MODE <slice -id>dot>mode-id> <cc-range> <semi-colon>
145
146
147
     <cc-range> ::=
         WITHIN <digits> <range-op> <digits>
148
149
     <probability-value-mode> ::=
150
151
         <lbra> <real-number> (, latent:yes|no, latent_prob:<real-number>)?<rbra>
         | <lbra> prob:<real-number> (, latent:yes|no, latent_prob:<real-number>)?<rbra>
152
     <probability-value-cc> ::=
154
         <lbra> <real-number> <rbra>
155
```

3.5 FEI semantics

3.5.1 Common Causes

The specification of CC is made of a set of Cause specifications. Each Cause is made of a set of fault modes each associated with a (discrete) time interval. The set of fault modes is partitioned wrt the modules they belong to.

In the example, the FEI contains a single Cause CC1, involving *all* instances of module Switch, and different fault modes dynamics for the Generator s.

```
COMMON CAUSES
1
     CAUSE CC1
2
           MODULE Generator
3
             FOR INSTANCES SC G1
4
             MODE Gen_StuckOff.stuckAt_Off WITHIN 0 .. 0;
5
6
           MODULE Generator
7
             FOR INSTANCES SC.G[23]
8
             MODE Gen_StuckOff.stuckAt_Off WITHIN 1 .. 3;
9
10
           MODULE Switch
11
             MODE Switch_StuckClosed_StuckOpen.stuckAt_Open WITHIN 2 ... 3;
12
             MODE Switch_StuckClosed_StuckOpen.stuckAt_Closed WITHIN 3 .. 4;
13
```

In the example, when fault CC1 occurs, SC.G1 immediately may stuck off, which lead to having both SC.G2 and SC.G3 fail stuck off after 1 to 3 steps. All Switch are also involved, as they may stuck to/from stuck open and stuck closed. The example is certainly a bit artificial, but the intention is use the example to informally present the semantics underlying the CC.

Each MODULE M specification contains a set of references to fault modes which were defined in the extension of module M. Each fault mode reference is associated with a (discrete) time interval which is the delay of the occurrence of that fault mode after the Common Cause happen. For example, fault SC.G1.Gen_StuckOff.stuckAt_Off may happen instantaneously (0 steps) after fault CC1 happen, while fault Gen_StuckOff.stuckAt_Off for instances SC.G2 and SC.G3 may happen non-deterministically 1 to 3 discrete steps after CC1.

Occurrence of a Fault Mode A fault mode *may happen* here means that after the CC happen, a fault mode may be not able to happen due to the history and modeling reasons. For example, suppose a valve may fail by exploding or stucking at closed, and suppose the GDM does not allow for switching from "exploded" mode to "stuck-at-closed" mode. Now a CC involving an exploded valve may still happen, although the fault mode "stuck-at-closed" will never happen as a consequence of the CC.

Multiple instances Each MODULE specification identifies a set of instances (all if instances are not explicitly identified). Notice that the set of instances of MODULE M is a subset of the set of extended instances of module M. As for the fault extension, the FOR INSTANCES specification contains a list of patterns (wildcard) to filter existing names. Empty sets are not allowed.

A module instance m of module M can occur only once in each Common Cause. In the example SC.G1 involved in MODULE at line 3, and cannot occur in any other MODULE of Cause CC1.

When Cause's MODULE specification involves more than one instances, the intended semantics is that the CC involves *all* specified instances at the same time. For example, MODULE specification at line 7 involves both SC.G2 and SC.G3. When at time *t* CC1 happen, in a time $t + \Delta$ with $1 \leq \Delta \leq 3$, $\Delta \in \mathbb{N}$, *all* instances SC.G2 and SC.G3 which can enter fault mode Gen_StuckOff.stuckAt_Off will fail with that fault mode ².

Multiple Fault Modes As mentioned, Cause's MODULE specification allows for multiple fault modes references. This is the set of fault modes (and their timings) which may follow the occurrence of the CC. Since all fault modes in a MODULE specification affect the same instances, constraints are automatically added during the extension to avoid that two or more incompatible fault modes are activated at the same time. For example, fault modes at lines 12 and 13 may happen for all Switch instances 2 to 3 steps after CC1 occurs ³. The automatically added constraints avoid a deadlock by constraining the modes not to occur at the same time step.

Name	Parameters ⁴	T	ransitions
		Entering	During
StuckAtByReference_I	term	next(varout) = next(term)	next(varout) = next(term)
StuckAtByReference_D ⁵	term	next(varout) = term	next(varout) = term
StuckAtByValue_I	term	next(varout) = next(term)	next(varout) = varout
StuckAtByValue_D	term	next(varout) = term	next(varout) = varout
Frozen		next(varout) = input	next(varout) = varout
NonDeterminismByReference_Num_I	min_bound, max_bound	<pre>next(varout) >= next(min_bound) & next(varout) <= next(max_bound)</pre>	next(varout) >= next(min_bound) & next(varout) <= next(max_bound)
NonDeterminismByReference_Num_D	min_bound, max_bound	next(varout) >= min_bound & next(varout) <= max_bound	next(varout) >= min_bound & next(varout) <= max_bound
$NonDeterminismByValue_Num_I$	min_bound, max_bound	<pre>next(varout) >= next(min_bound) & next(varout) <= next(max_bound)</pre>	next(varout) = varout
$NonDeterminismByValue_Num_D^6$	min_bound, max_bound	next(varout) >= min_bound & next(varout) <= max_bound	next(varout) = varout
NonDeterminismByReference_Bool		next(varout) = FALSE next(varout) = TRUE	next(varout) = FALSE next(varout) = TRUE
NonDeterminismByValue_Bool		next(varout) = FALSE next(varout) = TRUE	next(varout) = varout
Conditional_I	condition, then_term, else_term	<pre>next(varout) = (CONDITION_AT_ENTRANCE ? next(then_term) : next(else_term))</pre>	<pre>next(varout) = (CONDITION_AT_ENTRANCE ? next(then_term) : next(else_term))</pre>
Conditional_D	condition, then_term, else_term	<pre>next(varout) = (CONDITION_AT_ENTRANCE ? then_term : else_term)</pre>	next(varout) = (CONDITION_AT_ENTRANCE ? then_term : else_term)
	condition, then_term_1, else_term_1,	next(varout_1) = (CONDITION_AT_ENTRANCE ? next(then_term_1)	next(varout_1) = (CONDITION_AT_ENTRANCE ? next(then_term_1)
$Conditional Dual Outputs_I$	then_term_2, else_term_2	$: next(else_term_1)) \\ \& next(varout_2) = ($: next(else_term_1)) & next(varout_2) = (

3.6 The Faults Library

²Assuming there are no other Causes specified for the same instances

 $^{^3}Assuming the GDM allows for switching between modes <code>stuckAt_Open</code> and <code>stuckAt_Closed</code>, in one or both directions$

ConditionalDualOutputs_D	condition, then_term_1, else_term_1, then_term_2, else_term_2	CONDITION_AT_ENTRANCE ? next(then_term_2) : next(else_term_2)) next(varout_1) = (CONDITION_AT_ENTRANCE ? then_term_1 : else_term_1) & next(varout_2) = (CONDITION_AT_ENTRANCE ? then_term_2 : else_term_2)	CONDITION_AT_ENTRANCE ? next(then_term_2) : next(else_term_2)) next(varout_1) = (CONDITION_AT_ENTRANCE ? then_term_1 & next(varout_2) = (CONDITION_AT_ENTRANCE ? then_term_2 : else_term_2)
RampDown	decr end_value	next(varout) = input	<pre>next(varout) = case ramp_mode = RAMPING_DOWN : varout - decr; ramp_mode = RAMPING_DONE : varout; esac;</pre>
Inverted		next(varout) = !input	next(varout) = varout
StuckAtFixed		next(varout) = varout	next(varout) = varout
RandomByReference		TRUE	TRUE
RandomByValue		TRUE	next(varout) = varout
ErroneousByReference		next(varout) != next(input)	next(varout) != next(input)
ErroneousByValue		next(varout) != next(input)	next(varout) = varout
DeltaOutByReference	delta	$\frac{\text{next(varout)} < (\text{next(input)} - \text{delta})}{ \text{next(varout)} > (\text{next(input)} + \text{delta})}$	next(varout) < (next(input) - delta) next(varout) > (next(input) + delta)
DeltaOutByValue	delta	next(varout) < (next(input) - delta) next(varout) > (next(input) + delta)	next(varout) = varout
DeltaInRandomByReference	delta	next(varout) >= (next(input) - delta) & next(varout) <= (next(input) + delta)	$next(varout) >= (next(input) - delta) \\ \& next(varout) <= (next(input) + delta)$
DeltaInRandomByValue	delta	next(varout) >= (next(input) - delta) & next(varout) <= (next(input) + delta)	next(varout) = varout
DeltaInErroneousByReference	delta	next(varout) >= (next(input) - delta) & next(varout) <= (next(input) + delta) & next(varout) != next(input)	next(varout) >= (next(input) - delta) & next(varout) <= (next(input) + delta) & next(varout) != next(input)
DeltaInErroneousByValue	delta	next(varout) >= (next(input) - delta)& next(varout) <= (next(input) + delta)	next(varout) = varout

Table 3.1: Effect Modes

Name	Parameters	Transitions			
		Entering	During	Back	
Permanent		T: failure			
Transient		T: failure	G: !self_fix	T: self_fix	
SelfFixWithCounter	counter_max	T: failure	G: counter < counter_max	G: counter $>=$ counter_max	

Table 3.2: Local Dynamics

The Fault Library contains the models split for the effects and the local dynamics. The library is made of xml files and optionally correlated with SMV file to model the behaviours.

⁴Other than "varout" and "input"

 $^{^5\}mathrm{Formerly}$ StuckAt

⁶Formerly NonDeterminism

3.6.1Effect Model

Stuck-At By Reference

2

4

5

7

8

9

An example of an EM which models the effects of stucking-at a given term by reference⁷.

```
<effects_model name="StuckAtByReference_D">
     <values>
3
       <input reads="term" type="Any"
        desc="The value at which the output has to be stuck. Can be a constant or a variable."/>
       <output writes="varout" reads="input"
         desc="The output variable name that reads on the input one"/>
6
     </values>
     < effect >
       <entering type="smv" local="false">entering.smv</entering>
       <during type="smv" local="false">during.smv</during>
10
11
     </effect>
     < raw />
13
   </effects_model>
```

Notice that SMV code can be either inlined in the library xml specification, or can be imported from external files. In the example both entering and during specification refer external files.

The entering specification:

```
file: entering.smv
1
       varout: the output variable
2
       term: The value at which the output has to be stuck. Can be a constant or a variable
3
4
  next(varout) = term
```

The during specification:

```
file: during.smv
       varout: the output variable
2
3
   next(varout) = term
4
```

There are two different types of Stuck At By Reference fault mode:

- StuckAtByReference_I for modules with instantaneous reaction (Figure 3.6)
- StuckAtByReference_D for modules with delayed reaction (Figure 3.7)

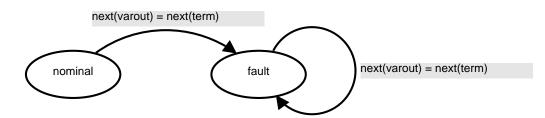


Figure 3.6: EM for a stuck-at instantaneous by reference fault mode

⁷By reference means that the value of varout will depend on the values of the parameters in real time

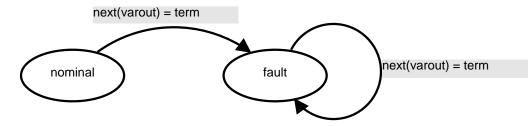


Figure 3.7: EM for a stuck-at delayed by reference fault mode

Stuck-At By Value

EM which models the effects of stucking-at a given term by value⁸.

- There is two different types of Stuck At By Value fault mode:
- StuckAtByValue_I for modules with instantaneous reaction (Figure 3.8)
- StuckAtByValue_D for modules with delayed reaction (Figure 3.9)

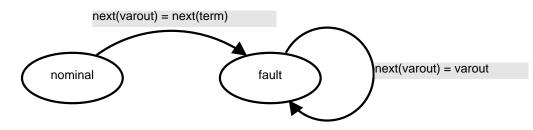


Figure 3.8: EM for a stuck-at instantaneous by value fault mode

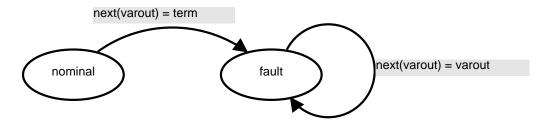


Figure 3.9: EM for a stuck-at delayed by value fault mode

Frozen

EM which models the effects of frozen to the last value.

Non Determinism By Reference for numeric variables

EM which models the effects of giving a random value between a min term and a max term by reference for numeric variables.

There is two different types of Non Determinism By Reference fault mode for numeric variables:

 $^{^{8}}$ By value means that the value of varout will be evaluated at the entrance in the fault mode, depending on the parameters, and remains the same for all the duration of the fault

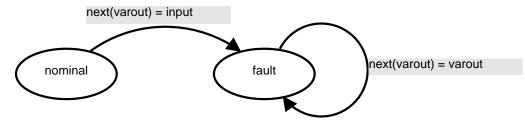


Figure 3.10: EM for a frozen fault mode

- NonDeterminismByReference_Num_I for modules with instantaneous reaction (Figure 3.11)
- NonDeterminismByReference_Num__D for modules with delayed reaction (Figure 3.12)

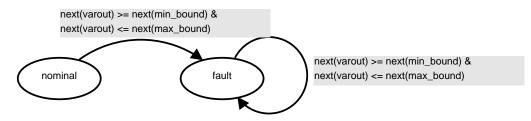


Figure 3.11: EM for a non determinism instantaneous by reference fault mode for numeric variables

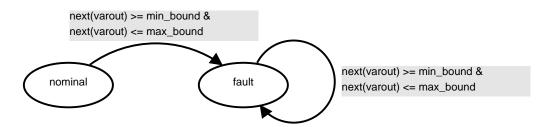


Figure 3.12: EM for a non determinism delayed by reference fault mode for numeric variables

Non Determinism By Value for numeric variables

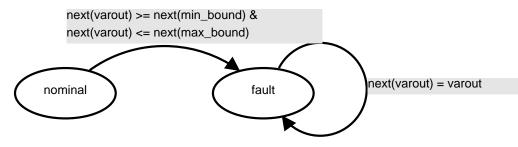
EM which models the effects of giving a random value between a min term and a max term by value for numeric variables.

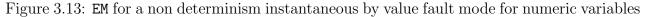
There is two different types of Non Determinism By Value fault mode for numeric variables:

- NonDeterminismByValue_Num_I for modules with instantaneous reaction (Figure 3.13)
- NonDeterminismByValue_Num__D for modules with delayed reaction (Figure 3.14)

Non Determinism By Reference for boolean variables

EM which models the effects of giving a random value by reference for boolean variables.





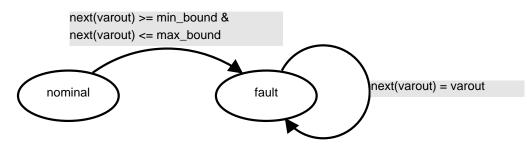


Figure 3.14: EM for a non determinism delayed by value fault mode for numeric variables

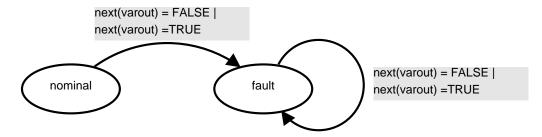


Figure 3.15: EM for a non determinism by reference fault mode for boolean variables

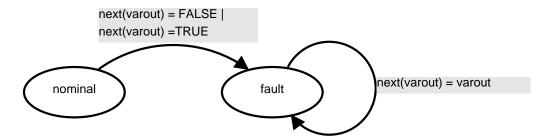


Figure 3.16: EM for a non determinism by value fault mode for boolean variables

Non Determinism By Value for boolean variables

EM which models the effects of giving a random value by value for boolean variables.

Ramp Down

2

3

4

An example of an EM which model the effects of ramping down a symbol.

```
<output name="varout" reads="input"/>
5
6
     </values>
7
     < effect >
       <entering>next(varout) = input</entering>
8
9
       <during>
          next(varout) = case
10
          ramp_mode = RAMPING_DOWN : varout - decr;
11
          ramp_mode = RAMPING_DONE : varout;
12
13
          esac
14
        </during>
15
     </effect>
16
     <raw>
17
       VAR ramp_mode : { RAMPING_DOWN, RAMPING_DONE };
       ASSIGN
18
19
        init(ramp_mode) := RAMPING_DOWN;
20
        next(ramp_mode) := case
           ! is_fault : RAMPING_DOWN;
21
           is_fault & varout - decr > end_value: RAMPING_DOWN;
22
23
           TRUE: RAMPING_DONE;
24
         esac;
25
      </raw>
   </effects_model>
26
```

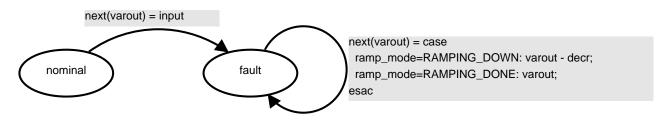


Figure 3.17: EM for a ramp-down fault mode

Notice keyword is_fault which is a predicate whose truth value is true iff the mode is fault. Conversely, predicate is_nominal which is true iff the mode is nominal.

Furthermore, in general the negation of is_fault is not equivalent to is_nominal, as when composed with other fms, both is_fault and is_nominal of a single EM can be false.

Conditional

An example of an EM which model the effects of choosing the varout value based on a boolean condition at the entrance.

```
<effects_model name="Conditional_D">
2
      <values>
         <input reads="condition" type="Boolean"/>
3
         <input reads="then_term" type="Any" />
<input reads="then_term" type="Any" />
<output reads="else_term" type="Any" />
<output writes="varout" reads="input" />
4
5
6
      </values>
7
      < effect >
8
         <entering type="smv" local="false">entering.smv</entering>
9
         <during type="smv" local="false">during.smv</during>
      </effect>
11
12
      <raw>
13
            VAR CONDITION_AT_ENTRANCE : boolean;
            ASSIGN
14
            init (CONDITION_AT_ENTRANCE) := condition;
            next (CONDITION_AT_ENTRANCE) := case
16
              !is_fault & next(is_fault) : CONDITION_AT_ENTRANCE;
17
               is_fault & next(is_fault): CONDITION_AT_ENTRANCE;
18
              TRUE : next(condition);
19
20
            esac;
```

21 </raw> 22 </effects_model>

There is two different types of Conditonal fault mode:

- Conditional_I for modules with instantaneous reaction (Figure 3.18)
- Conditional_D for modules with delayed reaction (Figure 3.19)

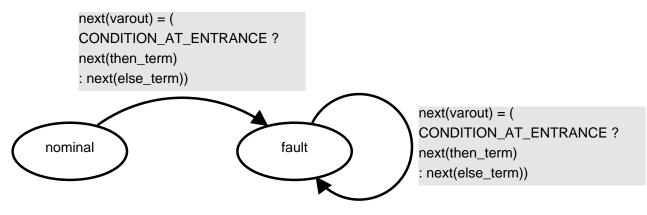


Figure 3.18: EM for a conditional instantaneous fault mode

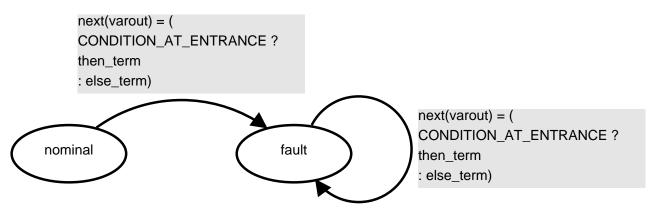


Figure 3.19: EM for a conditional delayed fault mode

ConditionalDualOutputs

An example of an EM which affect two outputs of the NC. Their values will be based on a boolean condition at the entrance.

```
<effects_model name="ConditionalDualOutputs_D">
2
       <values>
          <input reads="condition" type="Boolean"/>
3
 4
          <input reads="then_term_1" type="Any"
                                                             />
         <input reads="else_term_1" type="Any"/>
<input reads="then_term_2" type="Any" />
5
 6
                                                             />
          <input reads="else_term_2" type="Any" />
 7
         <output writes="varout_1" reads="input_1" />
<output writes="varout_2" reads="input_2" />
8
9
10
       </values>
       <\!{\rm effect}>
11
```

```
<entering type="smv" local="false">entering.smv</entering>
12
13
        <during type="smv" local="false">during.smv</during>
14
     </effect>
     <raw>
          VAR CONDITION_AT_ENTRANCE : boolean;
16
          ASSIGN
17
          init(CONDITION_AT_ENTRANCE) := condition;
18
          next(CONDITION_AT_ENTRANCE) := case
19
            !is_fault & next(is_fault) : CONDITION_AT_ENTRANCE;
20
21
            is_fault & next(is_fault): CONDITION_AT_ENTRANCE;
22
            TRUE : next(condition);
23
          esac;
24
      </raw>
25
   </effects_model>
```

There is two different types of ConditonalDualOutputs fault mode:

- ConditionalDualOutputs_I for modules with instantaneous reaction (Figure 3.20)
- ConditionalDualOutputs_D for modules with delayed reaction (Figure 3.21)

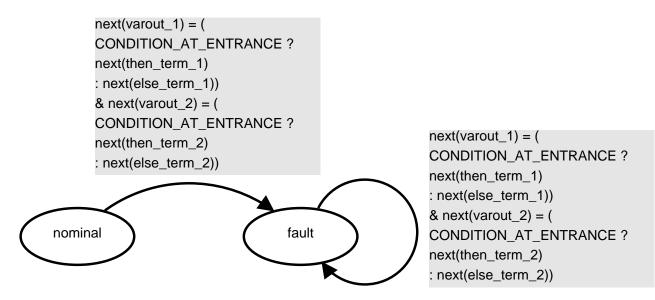


Figure 3.20: EM for a conditional instantaneous fault mode for two outputs

Inverted

EM which models the effects of being stuck at the inverted last value.

Stuck At Fixed

EM which models the effects of being stuck at a fixed random value. The differences with Non Determinism fault modes is that here the value to apply is randomly chosen at the beginning of the execution and never changed after.

Random by reference

EM which models the effects of giving a random value by reference.

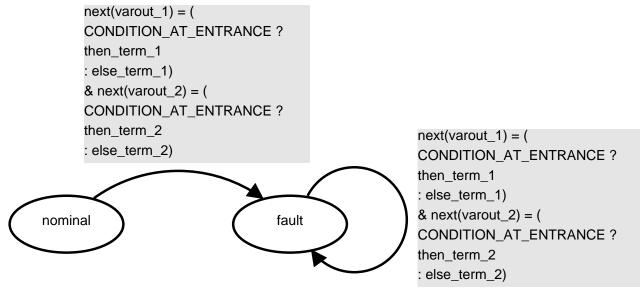


Figure 3.21: EM for a conditional delayed fault mode for two outputs

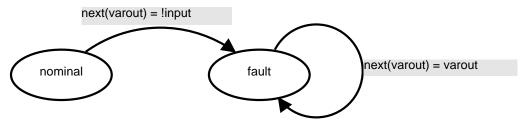


Figure 3.22: EM for an inverted fault mode

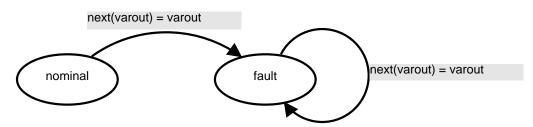


Figure 3.23: EM for an stuck at fixed fault mode

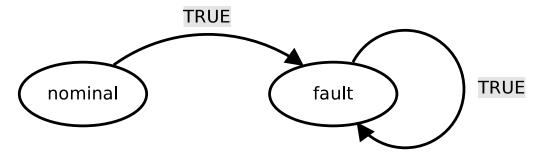


Figure 3.24: EM for a random by reference fault mode

Random by value

EM which models the effects of giving a random value by value.

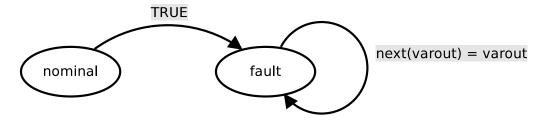


Figure 3.25: EM for a random by value fault mode

Erroneous by reference

EM which models the effects of giving an erroneous value by reference.

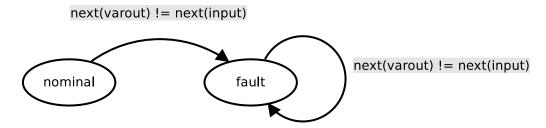


Figure 3.26: EM for an erroneous by reference fault mode

Erroneous by value

EM which models the effects of giving an erroneous value by value.

next(varout) != next(input)

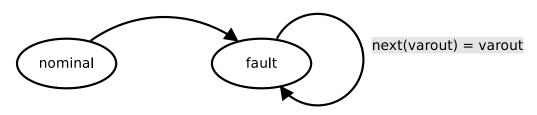


Figure 3.27: EM for an erroneous by value fault mode

Delta out by reference

EM which models the effects of giving a random value out of a delta range from the nominal value, by reference.

Delta out by value

EM which models the effects of giving a random value out of a delta range from the nominal value, by value.

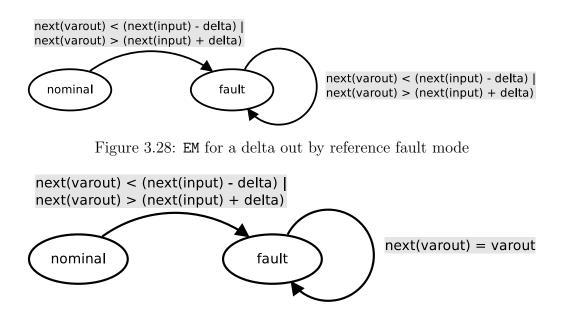


Figure 3.29: EM for a delta out by value fault mode

Delta in random by reference

EM which models the effects of giving a random value in a delta range from the nominal value, by reference.

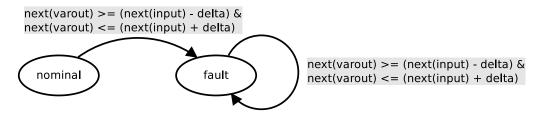


Figure 3.30: EM for a delta in random by reference fault mode

Delta in random by value

EM which models the effects of giving a random value in a delta range from the nominal value, by value.

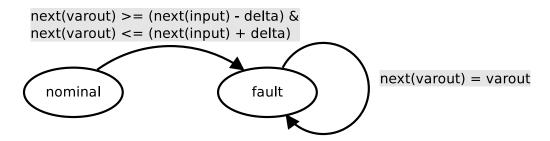


Figure 3.31: EM for a delta in random by value fault mode

Delta in erroneous by reference

EM which models the effects of giving an erroneous value in a delta range from the nominal value, by reference.

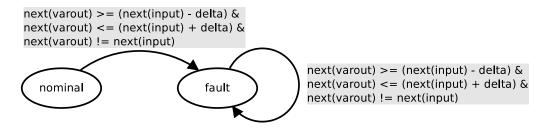


Figure 3.32: EM for a delta in erroneous by reference fault mode

Delta in erroneous by value

EM which models the effects of giving an erroneous value in a delta range from the nominal value, by value.

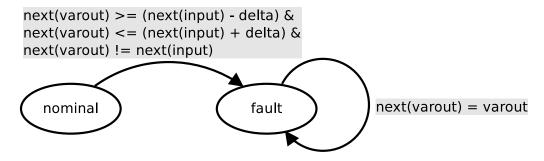


Figure 3.33: EM for a delta in erroneous by value fault mode

3.6.2 Local Dynamics

Example of a LDM which defines that a fault mode is reached when a failure event is nondeterministically issued, and it is transient as it can non-deterministically self-repair:

```
<local_dynamics_model name="Transient" desc="Bla bla">
       <templates>
2
           <template name="self_fix" type="Identifier">
3
             Description of the template.
4
           </template>
5
       </templates>
6
7
       <\!\mathrm{events}\!>
         <event type="input" name="failure"/>
<event type="input" name="${self_fix}"/>
8
9
       </events>
11
       <transitions>
         <transition from="nominal" to="fault">
12
13
            <trigger>failure</trigger>
         </transition>
14
         <transition from="fault" to="fault">
            <guard >!${ self_fix }</guard>
16
         </transition>
17
         <transition from="fault" to="nominal">
18
           <trigger>${self_fix}</trigger>
19
```

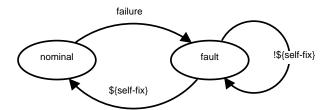


Figure 3.34: LDM for as self-repairing fault

Notice that the LDM is a template model which need to be instantiated later. nominal and fault are all reserved keywords. Like for s, keywords is_nominal and is_fault are available. Also notice that events admit negation (e.g. !event_name), meaning that the event is not being fired.

Chapter 4

Safety Assessment

Safety assessment allows the user to check the safety of the model, check how failure states can be reached, and which sequences of events can produce them. There are several forms of analysis, including:

- 1. Fault Tree Generation allows the user to generate a fault tree that shows the minimal sequence of events that may lead to a given undesired event (top-down analysis) see Section 4.2.
- 2. Failure Mode and Effects Analysis links combinations of events with the list properties that may be invalidated (bottom-up analysis) see Section 4.3.

4.1 Declaring the Fault Variables

Safety assessment can be applied to an .smv model, as long as a subset of the variables is identified as "fault variables". In case the automated fault extension is carried out, the fault variables are generated and identified automatically. On the other hand, in case the model is extended manually, the user has to specify the set of fault variables explicitly, as explained in Section 3.1. In both cases, the set of fault variables is stored as an XML file (compare Section 3.1.1).

The information on the set of fault variables is used by the underlying engines to carry out the analyses, e.g. minimal cut set computation. Fault variables are provided to the tool as an XML file. For technical reasons, such variables are *input* variables, and the tool generates a set of additional (*state*) variables that keep track of the activation of the fault variables (also called *history variables*, see [6, 5] for more details). Intuitively, a history variable becomes true as soon as the corresponding fault (variable) is triggered, and then it stays true permanently.

By default, xSAP constructs the history variables independently of the fault extension mechanism, i.e. with both the automated fault extension, and the manual fault extension. In the latter case, the user specifies an input model that is already extended and contains both the nominal and faulty behaviors.

The history variables may be already present in the extended model (e.g., in case they have been modeled manually by the user). In this scenario, they can be declared directly in the Fault Modes file, as explained in Section 3.1.1. There are two reasons why the user may want to declare them explicitly. First, for efficiency reasons. Second, when modeling and analyzing a static (combinational) system (i.e., a system with no transitions and only one

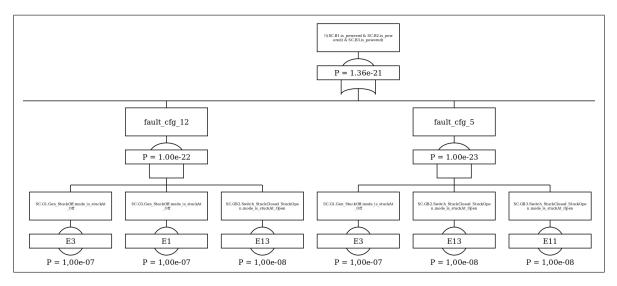


Figure 4.1: An example of fault tree

state). In this scenario, xSAP allows the user to model faults using a set of state variables that directly describe the failure states of the system, instead of modeling faults using input variables. Such set of state variables can play the role of the set of history variables.

Additional details on how to define the fault and history variables can be found in Section 3.1.1, whereas details about how to specify the fault variables, when invoking xSAP, can be found in Appendix D.1.

4.2 Fault Tree Generation

"Fault Tree Generation" constructs the fault tree for a given property. Basically, a fault tree is a collection of the minimal combinations of fault events that are associated with failure specified in the property.

The analysis can be done with or without hypothesis of monotonicity. Intuitively, the monotonicity hypothesis assumes that if failure is possible with a given fault configuration, then it is also possible for all the supersets. The algorithms for FT generation can be either based on BDD technology, or on SAT/SMT technology.

Remark: the algorithms based on SAT technology currently require BDDs to perform some internal manipulation of event combinations, hence, performance may be affected by the BDD variable ordering; if dynamic re-ordering of BDD variables is enabled and multiple fault trees are generated, the performance may be affected by the order in which fault trees are generated.

Once generated, the fault tree can be emitted in different output formats. In Figure 4.1 a snippet of fault tree is shown. In this example there are:

1. two branches (called *cut sets*), each of them representing a combination of three events (linked by an 'and' gate) that together cause the given failure state

The number of events in a cut set is called *order* of the cut set.

The probabilities of the root and the internal nodes of the fault tree are computed on the basis of the probabilities of the leaves, which are specified in the FEI input file.

Another form of analysis is the *dynamic* fault tree generation. The difference between the dynamic fault tree generation and the fault tree generation is that the "dynamic" one shows

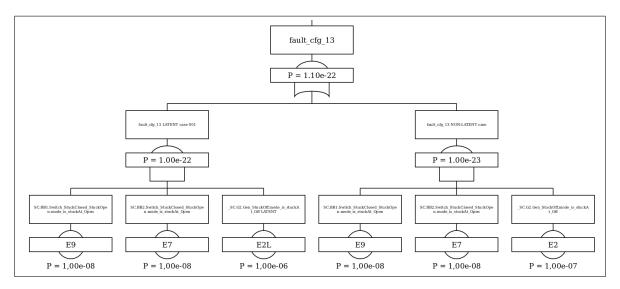


Figure 4.2: An example of fault tree with latent faults

also the precedence of the events (i.e., an event must hold before another one in order to reach the failure state) using the "priority and", in addition to the "and", gate in the fault tree.

4.2.1 Latent Faults

A fault can be declared to be possibly *latent*, that is, the corresponding item may be failed prior to the mission under analysis. Latent faults are specified in the FEI file, and associated with a latent probability. When latent faults are specified, they are taken into account in Fault Tree Generation, according to [25]. Namely, different cases (latent versus non-latent) are split and inserted in the fault tree. Figure 4.2 shows a snippet of a fault tree including latent faults.

4.3 Failure Modes and Effect Analysis

Failure Modes and Effect Analysis allows the user to generate, in tabular form, the set of combinations of events that may cause a given failure. If the *dynamic* option for the analysis is activated, then the order (i.e., the temporal dependence) of the events is considered important, otherwise, it is not.

The FMEA requires takes into account the properties to be checked, and the system fills a table with the results of the computation, that is then saved in various formats (e.g. commaseparated values)

As an example, an FMEA table is shown in Figure 4.3. The table contains an "ID" column that represents a unique identifier having the form N-M, where N identifies the fault configuration, and M the event it is associated with. The "ID" can be used to uniquely identify a fault configuration caused by a top level event (i.e., a single row in the FMEA table).

The highest computed cardinality can be specified in the "cardinality" field with command option -N. In the example this value is set to 2.

Nr	ID	Failure Mode	Failure effects
1	31 - 1	(_masterCCCC1.cc &	!((SC.B1.is_powered &
		SC.G3.Gen_StuckOff.mode_is_stuckAt_Off)	SC.B2.is_powered) &
			SC.B3.is_powered)
2	126-1	(_masterCCCC1.cc &	!((SC.B1.is_powered &
		SC.GB3.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Open)	SC.B2.is_powered) &
			SC.B3.is_powered)

Figure 4.3: An example of FMEA table

4.4 MTCS Analysis

Mode transition cut sets (MTCS) analysis is used to detect causes of system mode transitions, for example from an operational mode to a faulty mode with restricted functionality. The analysis is applicable on complex systems with various safety mechanisms such as redundancies and fault detection components. xSAP performs the analysis on an SMV model with a list of relevant events and a list of system modes.

The main challenge of the analysis is the formalization of the problem. Currently, xSAP implements two different approaches: strict and causal approach [7]. Both reduce the problem of finding causes to parameter synthesis problem and differ in the formula used for the parameter synthesis. The strict approach identifies events that happen in mode m_1 before transitioning to m_2 . The causal approach considers also events that can happen before m_1 but excludes those that are identified as a cause of m_1 . The latter approach is more sophisticated and makes use of MCS to compute causes of reaching m_1 .

The analysis can be run using the provided script compute_mtcs.py. As input, the script takes the SMV model, an XML file with state variables represententing events in the same format as in FT analysis, and a list of expressions representing system modes.

System modes are provided in one of two possible formats: either as a list of predicate formulas over state variables in the SMV model; or as a list of state variables with Boolean or enum type. In the former, each formula represents one mode. In the latter, modes are computed as a list of all evaluations of the given variables. Based on the provided options, the script either computes MTCS for all ordered pairs of distinct modes or for transitions from the first mode to all the other provided modes. If the modes are computed from state variables, the first computed mode is considered as the source mode.

The output is given as list of sets of events for each transition. Textual output is generated in a readable XML file. Visual output can be generated either in dot or tex format (using tikz pictures for visualization). Examples of the outputs are given in Section 7.3.8. For the visual outputs, paging option can be used to separate transition to individual dot files or individual pages of tex document. This improves the readability in case many transitions with many cut sets are visualized.

4.5 Common Cause Analysis

Common Cause Analysis (CCA) is an important step of safety assessment. Its purpose is to evaluate the consequences of events that may break the hypothesis of independence of different faults. For instance, an engine burst may case other components of an aircraft to break simultaneously. In such case, the probability of simultaneous failure of the components

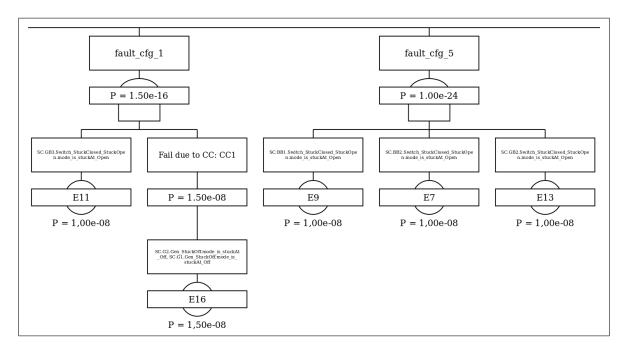


Figure 4.4: An example of fault tree with a common cause

is not given by the product of their failures as independent events – it is typically much higher.

CCA aims at investigating possible dependencies between failures, and evaluates the consequences in terms of system safety/reliability. Typically, a system design is evaluated with the goal of identifying common causes. Then, the consequences of common causes are analyzed. Finally, the impact on the design of the common causes is evaluated.

In xSAP, common cause analysis in carried out as part of Fault Tree Analysis. xSAP enables the definition of common cause events and of their consequences. Probability is attached to individual common causes, instead of (in addition to) their constituent faults as independent events. In xSAP a common cause may trigger the occurrence of a set of (dependent) faults in a user-specified manner (e.g., cascading or simultaneous).

xSAP enables the generation of FTs including common causes, and the evaluation of system reliability in presence of common causes. It is important to notice that failures due to common causes are analyzed together with component failures due to independent events (that is, independent failure of a component, and its failure due to a common cause, are both taken into account by the FT generation engine).

As an example, in Figure 4.4, the cutset on the left contains a reference to a common cause CC1. The CC is shown with the associated fault probability, and with information about the associated fault modes (Sc.G1 and Sc.G2 both eventually stuck at off). The cutset on the right instead shows a normal cutset containing three fault modes and no common causes.

Chapter 5

TFPG Analysis

Timed Failure Propagation Graphs (TFPG) were developed as a means to study the timed propagation of failures in complex systems [19, 18]. They represent a complementary approach to study the system's behavior under failures with respect to other safety assessment techniques such as FTA and FMECA. The focus of TFPGs is to describe the temporal dependence between a number of basic failure modes and a set of off-nominal system conditions (discrepancies) caused by the failure modes in a multi-mode (switching) system.

5.1 Timed Failure Propagation Graphs

A TFPG (Figure 5.1) consists of a labeled directed graph where nodes represent either failure modes, which are fault causes, or discrepancies, which are off-nominal conditions that are the effects of failure modes. Edges between nodes in the graph capture the effect of failure propagation over time in the underlying dynamic system. Edges in the graph model can be activated or deactivated depending on a set of possible operation modes of the system; this allows to represent failure propagation in multi-mode (switching) systems. Failure mode nodes do not have incoming edges since they are not bound to the occurrence of any other node; on the other hand, discrepancies must always be bound to the occurrence of failure modes, either directly or via intermediate discrepancies.

Time and system mode are two key factors in describing the propagation of a failure. In fact, a failure mode might lead to a chain of discrepancies that take time to occur, and a system mode might modify the way the failure propagates. For example, we might have a failure in a pipe, and this could propagate to another pipe; however, it might be the case that the two pipes are not connected (e.g., separated by a closed-valve) in the current system mode. This would mean that the failure cannot propagate from the first pipe to the second. For this reason, in a TFPG it is important to capture this information.

5.1.1 Terminology

We now fix the terminology used when talking about TFPGs as defined in [1]. Note that the original definition of TFPG did not include some features that were introduced in [1].

We say that a system may have different modes of operations, and we call them *system* modes. We assume that the system can only be in one system mode at the time and that,

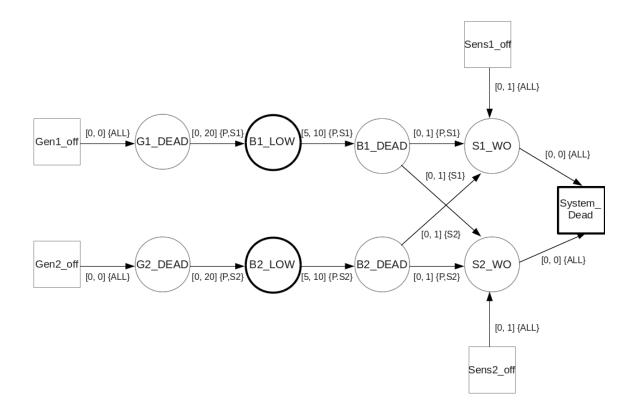


Figure 5.1: An example TFPG

given a finite amount of time, the system will change mode a finite amount of times, and that the system will stay in each mode for a non-zero amount of time.

A *failure mode* is a failure of a component of the system. A component might fail in more than one way, therefore it might have more than one failure mode. We call *fault* the occurrence of a failure of the component.

A fault in a component will produce anomalies in system behaviour. These anomalies are called *discrepancies*. We use the term *failure* to indicate a failure mode, a fault or a discrepancy.

5.1.2 TFPG Definition

We say that a TFPG is a structure $G = \langle F, D, E, M, ET, EM, DC, DS \rangle$, where:

- *F* is a non-empty set of failure modes;
- *D* is a non-empty set of discrepancies;
- $E \subseteq V \times V$ is a set of edges connecting the set of nodes $V = F \cup D$;
- M is a non-empty set of system modes. At each time instant t the system can be in only one mode;

- $ET: E \to I$ is a map that associates every edge in E with a time interval $[t_{min}, t_{max}] \in I$ indicating the min/max propagation time on the edge (where, $I \in \mathbb{R}_+ \times \mathbb{R}_+ \cup \{\inf\}$ and $t_{min} \leq t_{max}$)
- $EM : E \to \mathcal{P}(M)$ is a map that associates every edge in E with a set of modes in M. We assume that $EM(e) \neq \emptyset$ for any edge $e \in E$;
- $DC: D \rightarrow \{AND, OR\}$ is a map defining the class of each discrepancy as either AND or OR node;
- $DS: D \to \{M, I\}$ is a map defining the monitoring status of the discrepancy as either M for monitored or I not monitored (inactive).

A final assumption is that all and only failure modes can be root nodes: failure modes cannot have incoming edges, and discrepancies must have at least one incoming edge.

5.1.3 Semantics

The original semantics of TFPG [2] can be summarized as follows:

The state of a node indicates whether the failure effects reached this node. For a failure to propagate through an edge e = (v, v'), the edge should be active throughout the propagation, that is, from the time the failure reaches v to the time it reaches v'. An edge e is active if and only if the current operation mode of the system mc is in the set of activation modes of the edge, that is, $mc \in EM(e)$. For an OR-type node v' and an edge $e = (v, v') \in E$, once a failure effect reaches v at time t, it will reach v' at a time t', where $e.tmin \le t' - t \le e.tmax$ and the edge e is active during the whole propagation. On the other hand, the activation period of an AND alarm v' is the composition of the activation periods for each link $(v, v') \in E$. When a failure propagates to a monitored node v'(DS(v') = A), its physical state is considered ON; otherwise, it is OFF. If the link is deactivated any time during the propagation (because of mode switching), the propagation stops. Links are assumed memoryless with respect to failure propagation so that the current failure propagation is independent of any (incomplete) previous propagation. Also, once a failure effect reaches a node, its state will permanently change and will not be affected by any future failure propagation.

Infinity Semantics When defining the timing of a propagation, the use of ∞ can assume two different semantics: *open* and *closed*. The open semantics (written $[n, \infty)$) is used to describe the fact that the propagation time is finite, but unbounded. This means that we cannot chose a value *a-priori* to describe the maximum delay of the propagation. However, the propagation is guaranteed to occur. The closed semantics (written $[n, \infty]$) is used instead to describe propagations that might never complete (i.e., occur at infinity). This is used to capture propagation that are important, but might depend on external conditions not modeled by the TFPG (e.g., input from the environment).

xSAP only accepts TFPGs in which all infinities have an uniform semantics (being either open or close). This should be indicated in the TFPG model, for clarity. If not indicated, the default is *closed*. xSAP will inform the user if the chosen infinity semantics is not supported by the current analysis.

5.2 Reasoning Tasks

We now describe the reasoning tasks on TFPGs that are implemented in xSAP. The analyses are divided in two groups: *model-based* and *stand-alone*. The first group studies the TFPG in relation to a system model. This is used to verify that the TFPG captures the behaviors of interest, and to derive a TFPG from a system model. This group includes *Behavioral Validation*, *Synthesis*, and *Tightening*. The second group (stand-alone) aims at the study of the TFPG in isolation. It is used to prove properties on the TFPG, regardless of how the TFPG was obtained. This group includes *Possibility*, *Necessity*, *Activability and Consistency*, *Diagnosis*, *Refinement*, and *Filtering*.

The stand-alone analyses implemented in xSAP are based on [8]. These techniques are based on satisfiability modulo theory (SMT) reasoning, and provide a precise characterization of the timed behavior of the TFPG. Furthermore, they are limited to the *frozen-mode* assumption, i.e., they assume that the TFPG maintains the same mode throughout the execution.

5.2.1 Behavioral Validation

Behavioral validation is used to check if the TFPG is complete or incomplete with respect to a model [4]. This means to check whether every trace in the system model has a corresponding compatible trace in the TFPG.

This check is based on a number of "TFPG associations", which define each failure mode, discrepancy, and system mode in terms of propositional expressions over the system state variables. If a discrepancy d is not defined in the associations file, it is defined in terms of all nodes that have an edge towards it. The semantics of an AND node that doesn't have an associated expression is that it activates whenever all of its predecessors in the graph activate. The semantics of an OR node is that it activates whenever at least one of its predecessors has been activated. Such discrepancies are referred to as "virtual discrepancies".

A TFPG is considered to be incomplete with respect to the system if a system trace can be found that violates the TFPG constraints. This means that there are failure propagations in the system not captured by the TFPG. Specifically, a counterexample to TFPG completeness consists of a system trace and its interpretation in terms of the TFPG variables. The system trace is a valid trace for the system model, but its TFPG interpretation does not satisfy the TFPG constraints. This can be because a discrepancy fires when it shouldn't, for instance because none of its direct causes have occurred yet; on the other hand it can be because a discrepancy does not activate even though the TFPG constraints would require it to.

Such counterexamples can be used by the designer to get information about the missing behaviour in the TFPG, and possibly fix the TFPG or the system model, depending on where the inconsistency lies.

In addition to completeness, xSAP can also be used to test the tightness of a given TFPG w.r.t. a system model. Tightness is a property associated to the edge constraints: a TFPG is said to be tight if it is not possible for some edge to either increase its t_{min} , decrease its t_{max} , or drop any of the modes associated to the edge, without breaking completeness. In other words, a TFPG is tight if it is complete and propagations are possible in all modes and at the minimum and maximum propagation delay bounds.

We remark that the current implementation is sound but not complete for disproving the tightness of parameters $t_{max} = \infty$. Specifically, xSAP currently can identify only a subset of tightness witnesses for such assignment, and thus answer either *tight* or *unknown*.

5.2.2 Synthesis

TFPG Synthesis is used to automatically synthesize a complete TFPG, starting from a system model and a set of TFPG associations as described in Section 5.2.1. Specifically, the procedure computes the precedence constraints among all user-defined discrepancies and failure mode nodes and instantiates a respective TFPG. Where the user-defined nodes are not sufficient to express the Boolean constraints on precedences, virtual discrepancies as described in the previous section are introduced.

Note that TFPG synthesis only computes the underlying graph of the TFPG. The timings are overapproximated with tmin=0 and tmax=inf for all edges. Tight edge parameters can be identified with the tightening procedure described in Section 5.2.3.

By default the synthesis procedure analyzes, using cone-of-influence, the functional dependencies among nodes and uses this information to prune TFPG edges that represent purely temporal correlations among the events associated to nodes. This can be disabled if desired.

The following problems can appear during synthesis and will be reported to the user.

- Unreachable Discrepancy Within Analysis Bound: It states that a discrepancy is never activated in the model within the given bound. It is suggested to either increase the bound (if the discrepancy is expected to be activated) or remove the discrepancy from the list of associations (if it is indeed never activated). The discrepancy will still be placed in the resulting TFPG, isolated from the rest. This is for the user's convenience in debugging the problem, but results in an illegal TFPG.
- Correlated Discrepancy Activations: It states that there are discrepancy nodes that under certain or all failure configurations are always activated at the same time. In order to obtain a clearer graph structure it is suggested to re-run synthesis after dropping one of the correlated discrepancies from the TFPG association list given in input.
- Spontaneous Discrepancy Activation: It states that a case has been identified where a discrepancy node activated spontaneously, i.e. without any failure mode nodes being activated beforehand. In this case the user could redefine the discrepancy as a failure mode, investigate the expression over system state variables associated to it, or simply drop it from the problem input. The discrepancy will still be placed in the resulting TFPG, isolated from the rest. This is for the user's convenience in debugging the problem, but results in an illegal TFPG. Also note that certain TFPG simplification routines cannot be performed on such a TFPG, which can result in a graph with increased complexity.
- Isolated Failure Modes: When failure mode nodes appear isolated in the resulting TFPG, two cases are possible. One, the failure mode never occurs within the analysis bound. Two, the failure mode occurs but has no effect on the discrepancies within the analysis bound. The user is informed accordingly, and the failure mode node is in both cases placed into the final TFPG. The result in this case is a legal TFPG (as opposed to the case of spontaneous discrepancies), because failure mode nodes are allowed to occur spontaneously in the TFPG formalism.

5.2.3 Tightening

TFPG Tightening is used to improve the accuracy of the edge parameters in a TFPG. It takes as input a system model, association map, and a complete TFPG. The procedure tries to increase t_{min} values and decrease t_{max} values as much as possible, and drop as many mode labels as possible, without breaking the completeness of the given TFPG. A filter can be used to disable tightening of selected edge parameter classes.

Tightening of time bounds will be performed according to the highest precision of any of the time bound constants in the given TFPG. For instance, if the constant with the highest precision is 2.52, tightening considers 0.01 as the smallest possible search delta.

The t_{max} parameters that are set to $+\infty$, search for a corresponding finite tight value is limited to an upper bound. If no such bound is specified by the user, it is automatically set to the highest constant appearing in any edge constraint of the TFPG.

In cases where the user does not specify the input variable of the model encoding the time delta of each transition, only the mode labels are tightened.

5.2.4 Possibility, Necessity, Consistency, and Activability

The *Possibility* check enables the validation of a TFPG against a (partial) execution. This check can be used in different ways, in order to show additional properties of the TFPG, such as necessity, consistency and activability.

Given a TFPG and a (partial) trace of the TFPG (i.e., a set of active nodes, a set of activation times and, optionally, a mode), the *possibility check* verifies that the trace is compatible with the TFPG and provides a complete trace (i.e., a mode and an association of each node to a status and activation time). *Necessity* verifies that a partial trace is implied by the TFPG. This is usually the case when some behavior is intrinsic in the TFPG. *Consistency* is a special case of possibility, in which we check that there is at least one complete trace for the TFPG. *Activability* tests that every node can be eventually activated.

5.2.5 Refinement

When editing TFPGs, it is useful to understand the relation between the original and modified version of the TFPG. Given a (partial) mapping between the discrepancies of the original and modified TFPG, the refinement check verifies that all behaviors of the original TFPG can be mapped into behaviors of the modified one (i.e., the original TFPG *refines* the modified one).

5.2.6 Diagnosis

TFPG are commonly used to diagnose problems in systems. Given a set of timed observations, we can ask which Failure Modes could cause such a behavior. We implement two types of diagnosis. The first type simply enumerates all possible sets of failure modes (*diagnosis*) compatible with the observations. The second, instead, checks whether a given failure mode appears in all diagnosis (*certain diagnosis*). Certain diagnosis tells us whether we can be certain about the occurrence of the failure mode, given the observations. This type of diagnosis behaves particularly well with our symbolic SMT-based approach, and can be used to diagnose TFPGs with thousands of nodes.

5.2.7 Filtering

Finally, xSAP provides means to filter a given TFPG in the following two ways. First, the user might be interested in analyzing only part of a given TFPG, specifically in paths leading to selected TPFG nodes of interest. xSAP can remove all paths and nodes from the input TFPG from which the selected nodes cannot be reached. The result is a simpler more focused TFPG, on which manual inspection can be done more easily and automated analysis more efficiently. Second, when manually building a TFPG, the graph might contain redundant edges. xSAP provides the means to automatically remove such edges if present.

5.3 TFPG Formats

TFPG files and TFPG association files use two different types of format:

- Textual Format
- XML Format

5.3.1 Textual Format

We first describe the concrete syntax, i.e. the textual format.

TFPG file

The textual format for TFPG files (extension: .tfpg) makes use of the following keywords:

- "NAME": the name of TFPG
- "FM": the Failure Mode (FM node_name)
- "AND": the AND Discrepancy (AND node_name)
- "OR": the OR Discrepancy (OR node_name)
- "EDGE": the Edge (EDGE edg_name src_node dst_node min_time max_time edge_modes)
- "MONITORED": indicates if a discrepancy is monitored
- "INFINITY": indicates infinite time
- "*ALL*": all the modes
- "MODES": the Modes (mode_name)
- "INFINITY_SEMANTICS_OPEN", "INFINITY_SEMANTICS_CLOSED": indicates which semantics to use for ∞

We remark that:

• Comments are those lines starting with '--'

• Identifiers (name of the TFPG, name of a node, name of a mode and name of an edge) are sequences of the following characters: 'A'...'Z', 'a'...'z', '0'...'9', '_', '-'

From a high-level point of view, a structural TFPG consists of a list of nodes, which can be of three different types:

- Failure Modes
- Discrepancies:
 - And discrepancies
 - Or discrepancies

and of a list of modes and a list of edges connecting those nodes. Each Node is associated to:

- **Type:** the type of the node (Failure Mode, And Discrepancy, Or Discrepancy)
- Name: the name of the node
- IsMonitored: a value stating whether it is monitored or not

Each Edge is associated to:

- **SrcNode:** the name of the source node
- **DestNode:** the name of the destination node
- Name: the name of the edge
- **TMin:** the minimum time of the edge
- **TMax:** the maximum time of the edge
- Modes: a list of modes

An example of the textual file is given in Figure 5.2. A textual file can be written in free format. It must contain:

- 1. "Name" of the TFPG: it is the first (non-empty) token of the file, which consists of the keyword "NAME" followed by the name of the TFPG
- 2. (Optionally) The semantics used for the "*INFINITY*" operator (OPEN or CLOSED). If not specified, the default is Closed.
- 3. Definition of the "nodes". A node is defined through a keyword defining its type "(FM, AND or OR)" followed by its name and a value stating whether it is monitored or not. For instance, the following are possible definitions:

¹ FM Gen1_off

² AND System_Dead MONITORED

³ OR G1_DEAD

NAME Sensor-Generator 1 2 INFINITY_SEMANTICS_CLOSED 3 4 FM Gen1_off FM Gen2_off 6 FM Sens1_off 7 FM Sens2_off 8 AND System_Dead MONITORED 9 OR G1_DEAD 10 OR B1_LOW MONITORED 11 OR B1_DEAD 12OR S1_WO 13 OR G2_DEAD 14OR B2_LOW MONITORED OR B2_DEAD 16OR S2_WO 17 18 MODES Primary, Secondary1, Secondary2 19 20 EDGE EDGE1 Gen1_off G1_DEAD 0 INFINITY (Primary, Secondary1, Secondary2) 21 EDGE EDGE2 G1.DEAD B1.LOW 0 INFINITY (Primary, Secondary1) 22 EDGE EDGE3 B1_LOW B1_DEAD 0 INFINITY (Primary, Secondary1) 23 EDGE EDGE4 B1_DEAD S1_WO 0 INFINITY (Primary, Secondary1) 24EDGE EDGE5 S1_WO System_Dead 0 INFINITY (Primary, Secondary1, Secondary2) 25EDGE EDGE6 Gen2_off G2_DEAD 0 INFINITY (Primary, Secondary1, Secondary2) 26EDGE EDGE7 G2.DEAD B2LOW 0 INFINITY (Primary, Secondary2) 27 EDGE EDGE8 B2LOW B2DEAD 0 INFINITY (Primary, Secondary2) 28 EDGE EDGE9 B2_DEAD S2_WO 0 INFINITY (Primary, Secondary2) 29 EDGE EDGE10 S2-WO System_Dead 0 INFINITY (Primary, Secondary1, Secondary2) 30 EDGE EDGE11 B1_DEAD S2_WO 0 INFINITY (Secondary1) 31 EDGE EDGE12 B2_DEAD S1_WO 0 INFINITY (Secondary2) 32 EDGE EDGE13 Sens1_off S1_WO 0 INFINITY (Primary, Secondary1, Secondary2) 33 EDGE EDGE14 Sens2_off S2_WO 0 INFINITY (Primary, Secondary1, Secondary2) 34

Figure 5.2: TFPG textual format example

4. Definition of the "modes". This is a part related to the edge definition. Modes are introduced by the keyword "MODES" and are comma-separated, like for instance:

MODES Primary, Secondary1, Secondary2	
---------------------------------------	--

5. Definition of the edges. An edge is defined through a keyword ("*EDGE*"), followed by its name, the source node, the destination node, the minimum time, the maximum time and the list of the modes, defined as a comma-separated list encapsulated in parentheses. Also in this case, two choices as the ones presented for nodes are possible. For instance, the following are possible definitions:

1	EDGE	EDGE1	Gen1_o	ff G1_DEA	D	0 INFINIT	Y ALL	
2	EDGE	EDGE2	G1_DEAL	B1LOW	0	INFINITY	(Primary,	Secondary1)
3	EDGE	EDGE3	B1_LOW	B1_DEAD	0	INFINITY	(Primary,	Secondary1)

We remark that:

• Using the keyword "INFINITY" the maximum time can be defined to be infinity, e.g.

EDGE EDGE15 Gen1_off G1_DEAD 0 INFINITY (Primary)

- In the current implementation TMin values other than 0 are ignored and internally replaced by 0, and TMax values other than infinity are ignored and internally replaced by 0. Support of timing will be implemented in a future release.
- An edge can be active in all possible modes; in this case, instead of explicitly writing all the existent modes, the user can use the keyword "ALL", e.g.

EDGE EDGE13 Sensl_off S1_WO 0 INFINITY ALL

TFPG Associations file

The textual format for TFPG associations (extension: .tfpga) contains the following keywords:

- "FAILURE_MODES": to introduce the list of expressions related to failure modes.
- "MONITORED_DISCREPANCIES": to introduce the list of expressions related to monitored discrepancies.
- "UNMONITORED_DISCREPANCIES": to introduce the list of expressions related to unmonitored discrepancies.
- "MODES": to introduce the list of expressions related to the TFPG modes.

Each keyword is followed by the list of associations of that type; two assumptions are done in this case:

- 1. The identifier (i.e. the TFPG symbol) is the first word encountered in a line consisting of alphanumeric, underscore ('_') or minus ('-') characters.
- 2. The related expression is given by every character found starting from the identifier enclosed between the space character (' ') and newline ('backslash n').

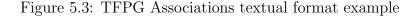
An example of this format is given in Figure 5.3.

5.3.2 XML Format

A sample TFPG file (whose name, by convention, ends with .txml) looks as follows:

```
<?rxml version='1.0' encoding='UTF-8'?>
<tfpg name="Sensor-Generator">
<nodesList>
<node name="Gen1_off" isMonitored="false">
<type>FM</type>
```

```
FAILURE_MODES
   Gen1_off root.sc_sys.sc_psu1.sc_generator.sc_errorSubcomponent.mode = error_dead
2
   Gen2_off root.sc_sys.sc_psu2.sc_generator.sc_errorSubcomponent.mode = error_dead
3
   Sens1_off root.sc_sys.sc_sensor1.sc_errorSubcomponent.mode = error_dead
4
   Sens2\_off \ root.sc\_sys.sc\_sensor2.sc\_errorSubcomponent.mode = \ error\_dead
6
   MONITORED_DISCREPANCIES
7
   B1_LOW root.sc_sys.sc_psul.sc_battery.data_low
8
   B2LOW root.sc_sys.sc_psu2.sc_battery.data_low
9
   System_Dead !root.sc_sys.data_is_alive
10
11
   UNMONITORED_DISCREPANCIES
12
   G2_DEAD root.sc_sys.sc_psu2.sc_generator.data_has_power = FALSE
13
   G1_DEAD root.sc_sys.sc_psu1.sc_generator.data_has_power = FALSE
14
   S1_WO root.sc_sys.sc_sensor1.data_reading = FALSE
15
  S2_WO root.sc_sys.sc_sensor2.data_reading = FALSE
16
  B1_DEAD root.sc_sys.sc_psu1.sc_battery.data_has_power_out = FALSE
17
   B2\_DEAD root.sc\_sys.sc\_psu2.sc\_battery.data\_has\_power_out = FALSE
18
19
  MODES
20
21
   Primary root.sc_sys.data_mode_selector = const_Primary
   Secondary1 root.sc_sys.data_mode_selector = const_Secondary1
22
   Secondary2 root.sc_sys.data_mode_selector = const_Secondary2
23
```



```
</node>
<node name="Gen2_off" isMonitored="false">
  <type>FM</type>
</node>
<node name="Sens1_off" isMonitored="false">
  <type>FM</type>
</node>
<node name="Sens2_off" isMonitored="false">
  <type>FM</type>
</node>
<node name="System_Dead" isMonitored="true">
  <type>AND</type>
</node>
<node name="G1_DEAD" isMonitored="false">
  <type>OR</type>
</node>
<node name="B1_LOW" isMonitored="true">
  <type>OR</type>
</node>
<node name="B1_DEAD" isMonitored="false">
  <type>OR</type>
</node>
<node name="S1_WO" isMonitored="false">
```

```
<type>OR</type>
  </node>
</nodesList>
<modesList>
  <mode>Primary</mode>
  <mode>Secondary1</mode>
  <mode>Secondary2</mode>
</modesList>
<edgesList>
  <edge name="EDGE1">
    <srcNode>Gen1_off</srcNode>
    <tMin>0.0</tMin>
    <tMax>-1.0</tMax>
    <modesList>
      <mode>Primary</mode>
      <mode>Secondary1</mode>
      <mode>Secondary2</mode>
    </modesList>
    <destNode>G1_DEAD</destNode>
  </edge>
  <edge name="EDGE2">
    <srcNode>G1_DEAD</srcNode>
    <tMin>0.0</tMin>
    <tMax>-1.0</tMax>
    <modesList>
      <mode>Primary</mode>
      <mode>Secondary1</mode>
    </modesList>
    <destNode>B1_LOW</destNode>
  </edge>
  <edge name="EDGE3">
    <srcNode>B1_LOW</srcNode>
    <tMin>0.0</tMin>
    <tMax>-1.0</tMax>
    <modesList>
      <mode>Primary</mode>
      <mode>Secondary1</mode>
    </modesList>
    <destNode>B1_DEAD</destNode>
  </edge>
  <edge name="EDGE4">
    <srcNode>B1_DEAD</srcNode>
    <tMin>0.0</tMin>
    <tMax>-1.0</tMax>
    <modesList>
      <mode>Primary</mode>
```

```
<mode>Secondary1</mode>
      </modesList>
      <destNode>S1_WO</destNode>
    </edge>
    <edge name="EDGE5">
      <srcNode>S1_WO</srcNode>
      <tMin>0.0</tMin>
      <tMax>-1.0</tMax>
      <modesList>
        <mode>Primary</mode>
        <mode>Secondary1</mode>
        <mode>Secondary2</mode>
      </modesList>
      <destNode>System_Dead</destNode>
    </edge>
  </edgesList>
</tfpg>
```

The value "-1" is used to symbolize infinity for TMax values.

A sample TFPG associations file (whose name, by convention, ends with .axml) looks as follows:

```
<?xml version='1.0' encoding='UTF-8'?>
<associations>
  <failureModes>
    <assoc id="Sens1_off"</pre>
           expr="root.sc_sys.sc_sensor1.sc_errorSubcomponent.mode = error_dead"/>
    <assoc id="Sens2_off"</pre>
           expr="root.sc_sys.sc_sensor2.sc_errorSubcomponent.mode = error_dead"/>
  </failureModes>
  <monitoredDiscrepancies>
    <assoc id="B1_LOW" expr="root.sc_sys.sc_psu1.sc_battery.data_low"/>
    <assoc id="B2_LOW" expr="root.sc_sys.sc_psu2.sc_battery.data_low"/>
    <assoc id="System_Dead" expr="!root.sc_sys.data_is_alive"/>
  </monitoredDiscrepancies>
  <unmonitoredDiscrepancies>
    <assoc id="S1_WO" expr="root.sc_sys.sc_sensor1.data_reading = FALSE"/>
    <assoc id="S2_WO" expr="root.sc_sys.sc_sensor2.data_reading = FALSE"/>
  </unmonitoredDiscrepancies>
  <tfpgModes>
    <assoc id="Primary" expr="root.sc_sys.data_mode_selector = const_Primary"/>
    <assoc id="Secondary1" expr="root.sc_sys.data_mode_selector = const_Secondary1"/>
    <assoc id="Secondary2" expr="root.sc_sys.data_mode_selector = const_Secondary2"/>
  </tfpgModes>
</associations>
```

Chapter 6

Fault Detection And Isolation

Fault Detection and Isolation (FDI) is concerned with diagnosing the faulty behavior of a system, identifying the specific fault that has occurred and designing a system able to activate suitable alarms whenever given faults occur.

This analysis can be organized in four different activities:

- **Diagnosability analysis** : the model of the system is analyzed to identify whether a specific fault (or combinations thereof) is diagnosable, i.e., there exists an ideal diagnoser that is always able to detect the fault.
- **Generation of minimum observables set** : this is an optional step in which the minimum set of observables required for detecting a specified fault is synthesized.
- Synthesis of diagnoser : using automatic synthesis techniques, a diagnoser is generated that raises an alarm every time a given fault occurs.
- **Effectiveness analysis** : the diagnoser is validated by model checking properties on the combined model (i.e., the system model + the model of the diagnoser). It is possible to use the diagnoser automatically generated with synthesis techniques, or manually design it.

6.1 Diagnosability Analysis

The purpose of diagnosability analysis is to verify whether enough observables are available to always detect a specific condition within a given time bound.

In order to perform this analysis we first need to specify the *diagnosis condition* of interest - for instance the occurrence of a fault - together with a desired on the *diagnosis delay bound*. Furthermore we need to specify a file specifying the *observable* variables needs to be given. These variables are the signals we can monitor in the system and which will be available to the diagnoser. Optionally, a *diagnosis context* can be specified to exclude certain traces that are unrealistic. This is relevant when the model doesn't include, for instance, precise description of the environment or of a controller for the modeled system. Note that if the condition is diagnosable under a context there is no guarantee w.r.t. behaviors outside the context. In practice this means that a diagnoser synthesized for the specification is guaranteed to raise the alarm only on traces covered by the context.

A counterexample to diagnosability, called *critical pair*, consists of a pair of traces that are observationally indistinguishable. On one trace the condition occurs, and on the other trace it doesn't (within the required time bound). Since the observations are the same on both traces, it is impossible for a diagnoser to distinguish them and thus raise the alarm with confidence that the fault actually happened.

A full description of the framework can be found in [9].

6.2 Generation of minimum observables set

The purpose of minimum observables set generation is to identify subsets of observables that still guarantee diagnosability of the chosen condition. This is helpful when trying to identify what sensors capture sufficient information, and in practice also to simplify the FDI implementation and possibly reduce its cost. This analysis is carried out by using the same inputs presented in diagnosability analysis section, i.e. a diagnosis condition, a delay bound, a set of available observables, and optionally a diagnosis context. Note that generation of minimum observables set is an optional step, which is not strictly required in order to complete FDI analysis.

6.3 Synthesis of diagnoser

The purpose of synthesis is to generate a diagnoser able to raise an alarm every time a specific fault occurs.

This analysis is carried out given a model of the system to be analyzed, the set of observables, and the specification of the alarms to be raised. In particular, if a fault is diagnosable in a given context, the synthesized diagnoser will be able to raise the alarm whenever the fault has occurred and the condition on the context (i.e., the indicator event) is satisfied.

The synthesis routines are based on the notions of:

- *Trace diagnosability*: the diagnosis is localized to individual traces (executions). The diagnoser will raise an alarm if it knows for sure that the fault has occurred in a specific execution, otherwise it will not.
- *Maximality of the diagnoser*: the diagnoser will raise an alarm *as soon as* it knows for sure that the fault has occurred.

Notice that we can specify many different alarms; a possible approach consists in adding an alarm for each different possible fault, and another one for a generic fault, represented by the disjunction of all possible faults.

6.4 Effectiveness analysis

Effectiveness analysis consists in the validation of the generated diagnoser.

The generated diagnoser is encoded in a module containing a state variable, representing the different states of it; additionally, three *DEFINE* constraints are contained, whose meaning is as follows:

- 1. *KFAULT*: represents the disjunction of those states where the diagnoser knows that the fault named FAULT has occurred.
- 2. *KnFAULT*: represents the disjunction of those states where the diagnoser knows that the fault named FAULT has not occurred.
- 3. UFAULT: represents the disjunction of those states where the diagnoser cannot decide whether the fault named FAULT has occurred or not.

By model checking a set of automatically generated LTL properties containing the previous *DEFINE* constraints, we can validate the generated diagnoser.

6.5 Files format

6.5.1 Observables file

The observables file simply lists (one per line) the variables representing the observable part of the system.

A sample observables file looks as follows:

```
    SC.G1.state
    cmd_G1
    SC.G2.state
    cmd_G2
```

Figure 6.1: Observables file example

6.5.2 Alarm Specification file

The alarm specification file lists the set of alarms to be considered. The suggested extension for these files is *.asl.*

Each alarm is defined with the following fields:

NAME The name of the alarm (mandatory)

CONDITION The diagnosis condition associated with the alarm (mandatory)

TYPE The type of the alarm: finite, bounded, exact (optional – defaults to finite)

DELAY The delay associated with the alarm. This makes sense only if type is bounded or exact.

CONTEXT The LTL context to consider when studying the alarm (optional)

NAME: A2 1 CONDITION: noise=1 2 3 NAME: A2 4 CONDITION: mb.ox 5TYPE: exact 6 7 DELAY: 1 8 NAME: A5 9 CONDITION: mb.ox 10 11 TYPE: bounded DELAY: 5 12CONTEXT: G F TRUE 13

Figure 6.2: ASL file example

Chapter 7

Triple Generator Example

In this document we use as a running example a model of a triple redundant generator. Its characteristics are described in the following sections.

7.1 Informal Description

7.1.1 The Plant

The triple generator example, represented in figure 7.1, is composed of several components which are described below:

- Generators (G1, G2 and G3): they have a single output link and can have two possible states:
 - "ON": the generator provides energy on the output link;
 - "OFF": the generator is switched off;
- Circuit Breakers (GB1, GB2, GB3, BB1, BB2 and BB3): they have two electric links and can have two possible states:
 - "OPEN": the circuit breaker maintains separated the two electric links;
 - "CLOSED": the circuit breaker connects the two electric links to each other;
- Buses (B1, B2 and B3): they represent the loads that can be powered by the generators. Each Bus has three links always connected each other so they must have the same value at each step;
- Controller: the component is in closed-loop with the system, which is sensed/controlled by the Controller itself.

System Faults

Both the generators and the circuit breakers change their internal state ("ON" or "OFF" for generators and "OPEN" or "CLOSED" for circuit breakers) according to the signal provided by the controller. This behavior is called nominal but it can diverge in case of failure as follows:

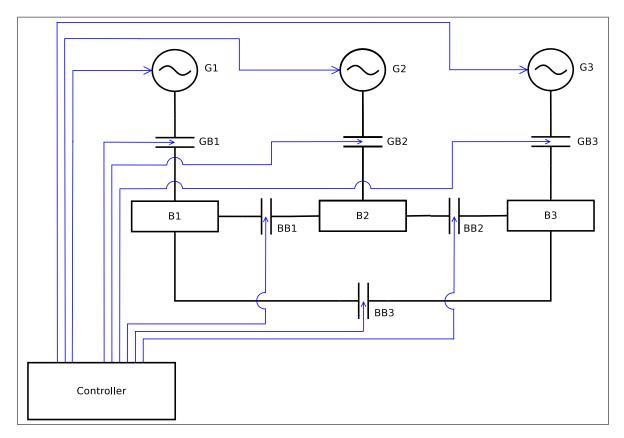


Figure 7.1: Triple Generator Example

- Generators can fail off permanently. It means that when the fault occurs the internal state of the generator is stuck at "*OFF*" permanently;
- Circuit breakers can fail open and closed. It means that, depending on the specific fault, the internal state of the circuit breaker is stuck at "*OPEN*" or "*CLOSED*" but the fault is not permanent, so the component can go back to the nominal behavior.

7.1.2 Controller behavior

Source to bus priority

The triple generator system has redundancies paths from the generators to each bus, this means that it is possible to provide the power in different way. In order to have a policy that privileges some power configuration respect to others they are defined some priority constraints on the Generator-to-Bus power, shown in table 7.1, and also the priority for each Generator-to-Bus path as depicted in table 7.2.

7.1.3 System Requirements

The behavior of the controller has to guarantee the observance of the following requirements:

- 1. No bus will be connected to more than 1 power source at any time.
- 2. If any power source is on, then all buses will be powered.

BUS	High priority	Medium priority	Low priority
B1	G1	G2	G3
B2	G2	G1	G3
B3	G2	G1	G3

Table	$7 1 \cdot$	Bus	nower	source	priority
Table	1.1.	Dus	power	source	priority

Source to bus paths	Priority	B1	B2	B3
G1	High	NA	BB1	BB3
	Low	NA	BB3-BB2	BB1-BB2
G2	High	BB1	NA	BB2
	Low	BB2-BB3	NA	BB1-BB3
G3	High	BB3	BB2	NA
	Low	BB2-BB1	BB3-BB1	NA

Table 7.2: Source to bus path priority

- 3. Bus power source priority and source to bus path priority schemes will be respected at all time (see tables 7.1 and 7.2).
- 4. If no power source is on, then all buses will be unpowered.
- 5. Any single/dual component failure will not cause other system requirements to be violated.
- 6. Never more than two generators on, unless required in case of failures.

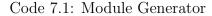
7.2 SMV modeling

The modeling of the Triple Generator system is composed of two main components: the controller and the system configuration.

The system configuration (see code 7.4) links together generators (code 7.1), switches (code 7.2) and buses (code 7.3) and it describes how they interact.

```
1MODULE Generator(cmd, init_state)
   VAR
2
     state : {on, off};
3
4
   DEFINE
5
     is_on := (state = on);
6
7
   DEFINE
8
9
     is_off := (state = off);
10
   ASSIGN
11
   next(state) :=
12
13 case
```

```
14 (cmd = cmd_on) : on;
15 (cmd = cmd_off) : off;
16 TRUE : state;
17 esac;
18
19 ASSIGN
20 init(state) := init_state;
```



```
1MODULE Switch (cmd, init_state)
   VAR
2
      state : {open, closed };
3
4
   DEFINE
5
     is_closed := (state = closed);
6
7
      is_open := (state = open);
8
   ASSIGN
9
    next(state) :=
10
11
       case
         (cmd = cmd_closed) : closed;
12
         (cmd = cmd_open) : open;
13
         TRUE : state;
14
15
       esac;
16
   ASSIGN
17
18
     init(state) := init_state;
```

Code 7.2: Module Switch

```
1MODULE Bus(in1, in2, in3)
   VAR
2
      state : {working, broken};
з
4
5
   DEFINE
      is_broken := (state = broken);
6
7
   DEFINE
      is_powered := (state = working) & (count((in1), (in2), (in3)) = 1);
8
9
      When the bus is overpowered it becomes broken and unfixable ----
10
   ASSIGN
11
      init(state) :=
12
        case
13
          (count((in1), (in2), (in3)) > 1) : broken;
14
          TRUE : working;
15
16
        esac;
17
      next(state) :=
18
19
        case
          next((count((in1), (in2), (in3)) > 1)) : broken;
20
          TRUE : state;
21
        esac;
22
```

Code 7.3: Module Bus

```
1MODULE System_Configuration (cmd_Gs, cmd_CBs)
     DEFINE
2
      init_G1 := on;
3
      {\tt init}_{-}G2 \ := \ {\tt on}\,;
4
      init_G3 := off;
5
6
      init_GB1 := closed;
7
      init_GB2 := closed;
8
      init_GB3 := open;
9
10
      init_BB1 := open;
      init_BB2 := closed;
11
      init_BB3 := open;
12
13
    VAR
14
      G1 : Generator(cmd_Gs[index_G1], init_G1);
15
      G2 : Generator(cmd_Gs[index_G2], init_G2);
16
17
      G3 : Generator(cmd_Gs[index_G3], init_G3);
18
      GB1 : Switch (cmd_CBs[index_GB1], init_GB1);
19
      GB2 : Switch (cmd_CBs[index_GB2], init_GB2);
20
      GB3 : Switch (cmd_CBs[index_GB3], init_GB3);
21
22
      BB1 : Switch(cmd_CBs[index_BB1], init_BB1);
BB2 : Switch(cmd_CBs[index_BB2], init_BB2);
23
24
      BB3 : Switch(cmd_CBs[index_BB3], init_BB3);
25
26
    --- Bus instances , see below for the input definition ---
27
28
      B1 : Bus(B1_poweredby_G1, B1_poweredby_G2, B1_poweredby_G3);
29
      B2 \ : \ Bus \big( \ B2\_powered by\_G1 \ , \ \ B2\_powered by\_G2 \ , \ \ B2\_powered by\_G3 \ ) \, ;
30
      B3 : Bus(B3_poweredby_G1, B3_poweredby_G2, B3_poweredby_G3);
31
32
    --- Definition of the signals (in matrix format) used by Controller
33
    --- to know the state of the single components
34
35
    DEFINE
36
      init_Gs := [
37
         init_G1,
38
39
         init_G2,
         init_G3];
40
41
      init_CBs := [
42
         init_GB1,
43
44
         init_GB2,
         init_GB3,
45
         init_BB1,
46
         init_BB2,
47
48
         init_BB3];
49
50
    DEFINE
      index_GB1 := 0;
51
      index_GB2 := 1;
52
      index_GB3 := 2;
53
54
      index_BB1 := 3;
55
      index_BB2 := 4;
56
57
      index_BB3 := 5;
58
      index_G1 := 0;
59
      index_G2 := 1;
60
61
      index_G3 := 2;
62
    DEFINE
63
      event_Gs := [
64
         fev_off_Gs,
65
         nev_Gs];
66
```

```
67
       event_CBs := [
68
         fev_open_CBs
69
70
         fev_closed_CBs ,
         nev_CBs];
71
72
73
    DEFINE
      fev_open_CBs := [
74
75
         GB1.fev_stuck_at_open ,
         GB2.fev_stuck_at_open ,
76
         GB3.fev_stuck_at_open ,
77
         BB1.fev_stuck_at_open ,
78
         BB2.fev_stuck_at_open,
79
80
         BB3.fev_stuck_at_open];
81
       fev_closed_CBs := [
82
         GB1.fev_stuck_at_closed ,
83
84
         GB2.fev_stuck_at_closed ,
         GB3.fev_stuck_at_closed,
85
86
         BB1.fev_stuck_at_closed ,
87
         BB2.fev_stuck_at_closed ,
         BB3.fev_stuck_at_closed];
88
89
       nev_CBs := [
90
91
         GB1.nev,
         GB2.nev,
92
93
         GB3.nev,
         BB1.nev,
94
         BB2.nev,
95
         BB3.nev];
96
97
    DEFINE
98
       fev_off_Gs := [
99
100
         G1.fev_stuck_at_off ,
         G2.fev_stuck_at_off,
101
102
         G3.fev_stuck_at_off];
103
       nev_Gs := [
104
         G1.nev,
105
         G2.nev
106
         G3.nev];
107
108
109
        Definition of the possible paths to Bus 1 -
110
111
    DEFINE
112
113
       B1_poweredby_G1_U := (G1.is_on) \& (GB1.is_closed);
       B1_poweredby_G2_L := B3_poweredby_G2_L & (BB3.is_closed);
114
       B1_poweredby_G2_R := B2_poweredby_G2_U & (BB1.is_closed);
115
       B1_poweredby_G3_L := B3_poweredby_G3_U & (BB3.is_closed);
116
       B1_poweredby_G3_R := B2_poweredby_G3_R & (BB1.is_closed);
117
118
        Definition of the possible paths to Bus 2 -
119
120
    DEFINE
121
122
       B2_poweredby_G1_L := B1_poweredby_G1_U & (BB1.is_closed);
       B2_poweredby_G1_R := B3_poweredby_G1_R & (BB2.is_closed);
123
       B2\_poweredby\_G2\_U := (G2.is\_on) \& (GB2.is\_closed);
124
       B2\_poweredby\_G3\_L := B1\_poweredby\_G3\_L & (BB1.is\_closed);
125
       B2_poweredby_G3_R := B3_poweredby_G3_U & (BB2.is_closed);
126
127
        Definition of the possible paths to Bus 3 -
128
129
    DEFINE
130
       B3_poweredby_G1_L := B2_poweredby_G1_L & (BB2.is_closed);
131
       B3_poweredby_G1_R := B1_poweredby_G1_U \& (BB3.is_closed);
132
       B3_poweredby_G2_L := B2_poweredby_G2_U & (BB2.is_closed);
133
```

```
B3_poweredby_G2_R := B1_poweredby_G2_R \& (BB3.is_closed);
134
       B3_poweredby_G3_U := (G3.is_on) \& (GB3.is_closed);
135
136
137
    -- Definition of the possible inputs for Bus 1 ---
138
139
    DEFINE
140
       B1_poweredby_G1 := B1_poweredby_G1_U;
141
142
       B1_poweredby_G2 := B1_poweredby_G2_R |
                                                   B1_poweredby_G2_L;
       B1_poweredby_G3 := B1_poweredby_G3_R | B1_poweredby_G3_L;
143
144
    -- Definition of the possible inputs for Bus 2 ---
145
146
    DEFINE
147
       B2_poweredby_G1 := B2_poweredby_G1_R \mid B2_poweredby_G1_L;
148
149
       B2_poweredby_G2 := B2_poweredby_G2_U;
       B2\_poweredby\_G3 := B2\_poweredby\_G3\_R | B2\_poweredby\_G3\_L;
150
151
    -- Definition of the possible inputs for Bus 3 --
152
153
    DEFINE
154
       B3_poweredby_G1 := B3_poweredby_G1_R \mid
                                                   B3_poweredby_G1_L;
155
       {\tt B3\_poweredby\_G2} \ := \ {\tt B3\_poweredby\_G2\_R} \ \mid
                                                   B3_poweredby_G2_L;
156
       B3_poweredby_G3 := B3_poweredby_G3_U;
157
```

Code 7.4: Module Circuit_System

A complete analysis of the Triple Redundant Generator example is out of the scope of this document. The full example is available in the xSAP tool distribution. In this manual, only some parts are reported, in order to illustrate the modeling in SMV language.

7.3 Concrete example of Fault Extension

7.3.1 Nominal Model

This example is taken from a larger model, but in this context only two modules are interesting: module Generator and module Switch.

Here a selected extract is presented, for seeing the complete example see: file examples/fe/triple_modular_generator/SC_TMG.smv and associated FEI file examples/fe/triple_modular_generator/SC_TMG.fei

Module Generator can break and can propagate a failure through event fev_stuck_at_off.

```
1MODULE Generator(cmd, init_state)
   VAR state : {on, off};
2
3
      Definition of the transition labels between nominal and fault state
4
   IVAR
5
     fev_stuck_at_off : boolean;
6
     nev : boolean;
7
8
   TRANS nev = FALSE;
9
10
11 DEFINE
     is_on := (state = on);
12
13
   DEFINE
14
     is_off := (state = off);
15
16
```

```
17 ASSIGN
18
    next(state) :=
       case
19
          (cmd = cmd_on) : on;
(cmd = cmd_off) : off;
20
21
          TRUE : state;
22
23
        esac;
24
25 ASSIGN
   init(state) := init_state;
26
```

Module Switch can break and can propagate a failure through events fev_stuck_at_closed and fev_stuck_at_open.

```
1 MODULE Switch (cmd, init_state)
2 VAR
     state : {open, closed };
3
4
5 --- Definition of the transition labels among nominal and fault states
6 IVAR
      fev_stuck_at_closed : boolean;
7
     fev_stuck_at_open : boolean;
8
     nev : boolean;
9
10
DEFINE is_closed := (state = closed);
DEFINE is_open := (state = open);
13
14 ASSIGN
15
   next(state) :=
16
      case
         (cmd = cmd_closed) : closed;
17
          (cmd = cmd_open) : open;
18
19
         TRUE : state;
20
       esac;
21
22 ASSIGN
23 init(state) := init_state;
```

7.3.2 Fault Extension Instruction

We want modules **Generator** and **Switch** to be affected by faults, for the moment with no common causes involved.

In particular, Generator shall be affected by a stuck-at effect (to off value), permanently. For this fault one StuckAtByValue_D fm with Permanent LDM does the job:

```
1 FAULT EXTENSION FE_SC_TMG
2 /-- ... --/
3
   EXTENSION OF MODULE Generator
4
5
       /-- Description of Fault Slice Gen_StuckOff --/
6
       SLICE Gen_StuckOff AFFECTS state WITH
7
8
          — Description of fault mode stuckAt_Off ——/
9
         MODE stuckAt_Off : Permanent StuckAtByValue_D(
10
                     data term << off ,</pre>
11
12
                     data input << state,
13
                     data varout >> state
                     event failure >> fev_stuck_at_off);
14
```

Here data value is assigned to constant off, and Generator's variable state is the AS, and within the fm it is read through input and written through varout.

LDM's events failure is bound to Generator's event fev_stuck_at_off, and template self_fix is bound to Generator's event nev.

Notice that parameters are bounded to the corresponding affected symbols by using the read operator << and the write operator >>:

1 data input << state 2 data varout >> state

In this example, value of variable state in the NC is bounded to read value input and write value varout. This means that current value of state can be read through input, and can be written through varout.

Templates are instantiated by using the = operator.

Module Switch shall be affected either by a stuck-at-closed or by a stuck-at-open faults. This requires two fms both transient. Furthermore, a GDM is defined to make possible move from stuck-open to stuck-closed by issuing event failure in fm stuckAt_Closed.

```
1FAULT EXTENSION FE_SC_TMG
2 /-- ... --/
3
   EXTENSION OF MODULE Switch
4
5
        -- Description of Fault Model for Switch --/
6
       SLICE Switch_StuckClosed_StuckOpen
7
        AFFECTS state WITH
8
9
          /-- Description of fault mode StuckAt_Closed --/
10
          MODE stuckAt_Closed : Transient StuckAtByValue_D(
11
                     data term << closed ,</pre>
12
                     data input << state.
13
                     data varout >> state,
14
                     template self_fix = self_fixed ,
15
16
                     event failure >> fev_stuck_at_closed ,
                     event self_fixed >> nev);
17
18
          /-- Description of fault mode StuckAt_Open --/
19
```

20	MODE stuckAt_Open : Transient StuckAtByValue_D($data$ term << open,
21	data input << state,
22	data varout >> state,
23	<pre>template self_fix = self_fixed ,</pre>
24	<pre>event failure >> fev_stuck_at_open ,</pre>
25	<pre>event self_fixed >> nev);</pre>
26	
27	GLOBAL DYNAMICS
28	/ Transition of FM Global Dynamics/
29	TRANS stuckAt_Open.fault $-[stuckAt_Closed.failure]->$ stuckAt_Closed.fault;

Code 7.5: FEI for the Switch module

Here two fms are defined, and notice the particular way transitions in the GDM are defined. There are two special keywords nominal and fault which identify the corresponding locations in each fm.

7.3.3 Modules and Module Instances in FEI

In the previous FEI example, *all* instances of modules **Generator** and **Switch** were affected. If we want to affect a subset of their instances (or different instances in different ways), construct FOR INSTANCES has to be used:

```
1 FAULT EXTENSION FE_SC_TMG
   EXTENSION OF MODULE Generator
3
4
       /-- affects only SC.G1 and SC.G3 --/
5
       FOR INSTANCES SC.G[13]
6
7
8
9
10
  EXTENSION OF MODULE Switch
11
12
       SLICE Switch_StuckClosed_StuckOpen
13
14
            --- affects SC.GB1, and all SC.BBs (SC.BB1, SC.BB2, SC.BB3) ---/
15
          FOR INSTANCES SC.GB1, SC.BB*
16
          AFFECTS state WITH
17
18
```

When specifying instances, list can be provided, and each entry may contain wild-card characters (*?[...]).

Instances can be defined at two different levels:

- **Module** (above, done for **Generator**) Affects specified instances for all slices which do not specify instances explicitly.
- Slice (above, done for Switch) Affects specified instances for a single slice, *overriding* any instance specification done for the containing module (if any).

7.3.4 Properties

With reference to the example proposed in Chapter 7, the requirements violation can be checked by defining some properties. We illustrate some examples below.

R2: if any power source is on, then all buses will be powered This requirement can be translated into an INVARSPEC as follows:

(G1.is_on | G2.is_on | G3.is_on) ->

(B1.is_powered & B2.is_powered & B3.is_powered)

R4: if no power source is on, then all buses will be unpowered This requirement can be translated into an INVARSPEC as follows:

 $(G1.is_off \& G2.is_off \& G3.is_off) \rightarrow$

(!B1.is_powered & !B2.is_powered & !B3.is_powered)

7.3.5 Formal properties

As described in the NUXMV user manual [20], the property definition is supported at modeling level in the following format:

[INVAR,LTL,CTL]SPEC NAME <property_name> := <property>;

7.3.6 Choose Fault Templates

With reference to the example proposed in Chapter 7, there is the following faults situation:

- Generators can fail off permanently;
- Circuit Breakers can fail open or closed transitorily.

This can be obtained by modeling two Fault Slices, one for each affected variable. With reference to the faults library defined in the appendix 3.6, the parameters needed by the faults library used to cover the faulty behavior of Generators and Circuit Breakers are represented in tables 7.3 and 7.4. The Fault Slice of Generators has a single Fault Mode, as depicted in table 7.3, and this implies that it is not necessary to have a Global Dynamics description.

Differently from the Slice Model of Generators, the Circuit Breakers need the definition of two Fault Modes, one for each faulty behavior (fail open and fail closed) as depicted in figures 7.2(a) and 7.2(b). The transient dynamics for Circuit Breakers imposes to have an event that occurs when the system goes back to nominal case. In the case proposed in figure 7.2 that event is called "self_fix_event" and it can be used at controller level if the controller has a full observability of the system state and configuration, and this is the case proposed in the Triple Generator Example (see Chapter 7).

Module	Affected Variable	Effect Model		Local Dynamics
Generator	state	StuckAtByValue_D	OFF	Permanent

Table 7.3: Fault Modes of the Generator

With reference to the example proposed in Chapter 7, in Figure 7.1, FEI code in 7.5 shows the Fault Slice of the Circuit Breaker.

Module	Affected Variable	Effect Mod	lel	Local Dynamics
Circuit Breaker	state	StuckAtByValue_D	OPEN	Transient
Circuit Breaker	state	StuckAtByValue_D	CLOSED	Transient

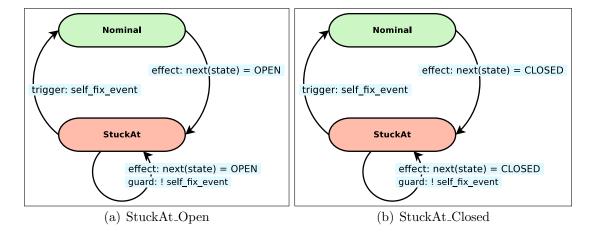
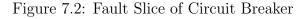


Table 7.4: Fault Modes of the Circuit Breakers



7.3.7 Result of Fault Extension

To create the extended model, run the Model Extender script:

```
$> cd examples/fe/triple_modular_generator/
$> python ../../.scripts/extend_model.py -v \
SC_TMG.smv SC_TMG.fei
INFO:root:Generated XML fei: out/SC_TMG.xml
INFO:root:Generated expanded XML fei: out/expanded_SC_TMG.xml
INFO:root:Generated expanded XML fei: out/expanded_SC_TMG.xml
INFO:root:Generated xsap commands script: out/xsap_extend_model.cmd
INFO:root:Invoking xsap to carry out smv extension of SC_TMG.smv
INFO:root:Successfully created:
INFO:root: - Extend smv file: 'out/extended_SC_TMG.smv'
INFO:root: - Fault modes xml file: 'out/fms_SC_TMG.xml'
```

Script extend_model.py has option -d which allows to specify the output directory to put all generated files into (default: ./out).

As shown by the verbose messages (enabled by option -v), generated files are:

- file out/extended_SC_TMG.smv
- file out/fms_SC_TMG.xml

Both will be used to perform the Safety Assessment Analysis in the following section.

7.3.8 Safety Assessment

Generating Fault Trees

In this example, the good property we want to study is "All of the buses B1, B2 and B3 are powered.", which corresponds to a TLE "At least one bus B1, B2 or B3 is not powered." To generate a fault tree, some options are available. Here is an instance:

Remark: notice the use of single straight quotes to protect special characters for bash shell (e.g. character !). Other shells may require different quotation characters.

The generated fault tree can then be shown with the Fault Tree Viewer:

```
$> python .../.../scripts/view_ft.py -v \
    --events-file out/extended_SC_TMGevents.txt \
    --gates-file out/extended_SC_TMGgates.txt \
```

The Fault Tree Viewer opens and shows the fault tree: Figure 7.3 shows one cut set where:

- G1 is stuck-at-off
- $\bullet~$ GB2 is stuck-at-open
- GB3 is stuck-at-open

In this scenario, none of the buses B1, B2 and B3 is powered.

Many cut sets are found (one is depicted in figure 7.3). Details can be found in the output provided by xSAP which is run by the script.

A total of 26 fault trees are found, and the minimal cardinality is 3 (14 minimal cut sets with cardinality 3, and 12 with cardinality 4):

Order	Number	Cumul
0	0	0
1	0	0
2	0	0
3	14	14
4	12	26

Command compute_ft.py offers many options, and in particular it can be used with different engines. It can also show (-s) the generated fault tree by invoking the Fault Tree Viewer automatically when done.

Execute compute_ft.py -h to see all available options.

Generating FMEA Tables

Generating a FMEA table takes one or more TLEs as inputs, and produces one file in CSV format, with tab as separator among fields.

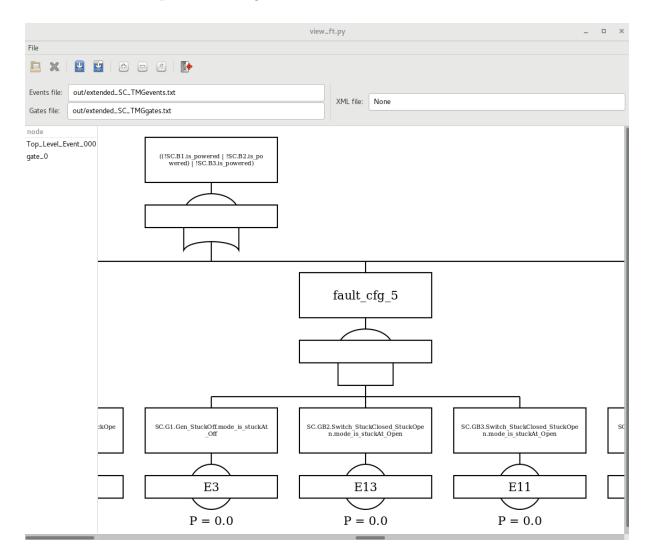


Figure 7.3: Fault Tree for TLE: "At least a bus is not powered"

There are four fields:

1. Incremental number of the cut set

- 2. ID of the cut set
- 3. Failure Modes
- 4. TLE

In our example, the property set is limited to the same TLE we used for generating the fault tree: "At least one bus B1, B2 or B3 is not powered."

We assume that the extended model has been already built into directory ./out as done in previous steps.

From the Fault Tree, we already know that the minimal fault cardinality is 3, however we begin with a cardinality 1 (option -N):

The command generates an empty FMEA table as expected. Now we will increase the cardinality to 3, and ask to show the table when done (-s):

Notice that the number of cut sets is equal to the number of cut sets found in previously computed fault tree with cardinality 3. This happens since a FMEA table with cardinality N includes the table with cardinality N-1, and since the FMEA tables with cardinalities 1 and 2 are empty.

This is the result of increasing the cardinality to 4:

```
INFO: root : Generated xsap commands script : out/xsap_compute_fmea.cmd
INFO: root : Invoking xsap to compute fmea table
INFO: root : xsap produced fmea table in: 'out/extended_SC_TMGfmea_table.txt '
ID: 1
TLE: ((!SC.Bl.is_powered | !SC.B2.is_powered) | !SC.B3.is_powered)
FMS: (SC.G1.Gen_StuckOff.mode_is_stuckAt_Off = TRUE & \
SC.G3.Gen_StuckOff.mode_is_stuckAt_Off = TRUE & \
SC.G3.Gen_StuckOff.mode_is_stuckAt_Off = TRUE & \
SC.G3.Gen_StuckOff.mode_is_stuckAt_Off = TRUE \
SC.G3.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Open = TRUE \)
SC.G3.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Open = TRUE \
SC.G3.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Open = TRUE \
SC.G3.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Open = TRUE \)
SC.G3.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Open = TRUE \
SC.G3.Switc
```

Total number of cut sets increased to 179, while for fault tree it was 26 (14 + 12). This is because FMEA tables are not minimal like cut sets in fault trees, and in particular they consider all possible faults even when (some) may be not really causing the TLE directly. For example with cardinality 4 you will find cut sets containing 3 faults causing the problem, and a fourth fault not really contributing, like a stuck-at-closed fault for a switch:

```
ID: 91
TLE: ((!SC.Bl.is_powered | !SC.B2.is_powered) | !SC.B3.is_powered)
FMS: (SC.GB1.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Closed = TRUE & \
    (SC.GB3.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Open = TRUE & \
    (SC.G1.Gen_StuckOff.mode_is_stuckAt_Off = TRUE & \
    SC.G2.Gen_StuckOff.mode_is_stuckAt_Off = TRUE)))
```

E.g. in the found cut set the fault:

SC.GB1.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Closed

does not contribute to the TLE as generator G1 is broken off anyway.

As for compute_ft.py, command compute_fmea_table.py offers many options, and in particular it can be used with different engines. Execute compute_fmea_table.py -h to see all available options.

Generating MTCS

We show the computed MTCS for some selected mode transitions in the running example of the triple generator model. As system modes we consider all configurations of the generator modes. That is, for each G_1 , G_2 , G_3 we get state either on or off. There are 8 system modes in total, hence 56 transitions of distinct system modes. The SMV model is extended with a new state variable mode, whose value corresponds to the actual combination of generator modes.

The xSAP command compute_mode_transition_cut_sets is called by a provided python script compute_mtcs.py. Examples for running this python script are in Listings 7.4, 7.5, 7.6. In Listing 7.4, all 56 transitions are analyzed and output is written in output.xml file in a readable form, and in output.tex file, where each transition is visualized on a separate page. The tex file needs to be built by LuaLaTeX to generate the output.pdf file with 56 pages. In Listing 7.5, only one transition is analyzed. Again, XML and tex output are generated. The visualized MTCS for this transition is shown in Figure 7.7(a). In Listing 7.6, two transitions are analyzed. The textual XML output is shown in Listing 7.8 and dot visualization in Figure 7.7(b).

```
$> python compute_mtcs.py ---smv-file extended_SC_TMG.smv
--fms-file fms_SC_TMG.xml
-V -g -o tex --expressions mode
```

Figure 7.4: Example of a command to compute MTCS for all mode transitions. Option -V specifies that expression is a variable.

```
$> python compute_mtcs.py --smv-file extended_SC_TMG.smv
--fms-file fms_SC_TMG.xml
-g -e -o tex
--expressions "mode_=_on_on_off" "mode_=_on_off_off"
```

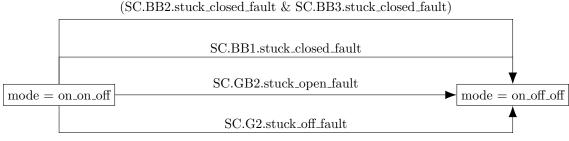
Figure 7.5: Example of a command to compute MTCS for the mode transition from on_on_off to on_off_off. -e specifies that only transitions from the first mode to all the other are considered.

Figure 7.6: Example of a command to compute MTCS for the mode transition from on_on_off to on_off_off and the mode transition from on_on_off to off_off_on.

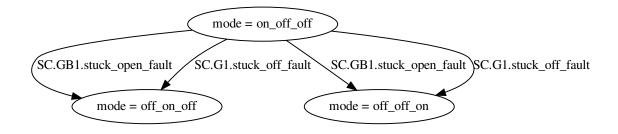
7.3.9 Adding Common Cause

We now add CC behaviour, by adding three CC to our FEI specification:

```
COMMON CAUSES
1
2
3
      ^{\prime}/-- Failure of G1 followed by failure of G3, which lead to a
4
          switch failure --/
5
      CAUSE CC1
6
7
           MODULE Generator
             FOR INSTANCES SC.G1
8
             MODE Gen_StuckOff.stuckAt_Off WITHIN 0 .. 0;
9
10
           MODULE Switch
11
             FOR INSTANCES SC. GB2
12
             MODE Switch_StuckClosed_StuckOpen.stuckAt_Open WITHIN 3 .. 5;
13
14
           MODULE Generator
15
             FOR INSTANCES SC.G3
16
             MODE Gen_StuckOff.stuckAt_Off WITHIN 1 .. 3;
17
18
19
      /-- Instantaneous failure of generators G1 and G2
20
      CAUSE CC2
21
           MODULE Generator
22
             FOR INSTANCES SC.G[12]
23
             MODE Gen_StuckOff.stuckAt_Off WITHIN 0 .. 0;
24
25
26
       --- Simultaneous failure of all generators
27
```



(a) Visual tex output of MTCS found by command in Listing 7.5.



(b) Visual dot output of MTCS found by command in Listing 7.6.

Figure 7.7: Example of visual outputs of MTCS.

```
    28 CAUSE CC3
    29 MODULE Generator
    30 MODE Gen_StuckOff.stuckAt_Off WITHIN 1 .. 2;
```

Cause CC1 refers two Generators and one Switch, supposing that a failure break off of instance G1, may lead to a failure of G3 as well in 1 to 3 steps, which may lead to a failure of switch GB2 in the following 3 steps (3..5).

Cause CC2 refers two Generators (G1 and G2), meaning that both may be involved in a single failure called CC2.

Cause CC3 models a simultaneous failure of all instances of module Generator, as the MODULE part of the CC specification does not specify the instances explicitly, *all* instances which have been extended are intended to be affected together.

When dealing with the TLE used in the example ("At least one bus B1, B2 or B3 is not powered"), notice that:

- CC1 produces a cut set with cardinality 1, as it breaks all power lines from generators.
- CC2 produces two cut set with cardinality 2, as a stand-alone failure to the line coming from G3 is needed together with CC2.
- CC3 produces a cut set with cardinality 1, as all generators are involved.

By adding only CC2 to the specification, this is the generated FMEA table:

```
< mtcs >
1
             <transition>
2
                      <from_mode>mode = on_off_off</from_mode>
3
                       <to_mode > mode = off_on_off </to_mode>
4
                       < cutsets >
5
                                <cutset>
6
                                          <event>SC.GB1.stuck_open_fault</event>
7
8
                                </\mathrm{cutset}>
                                <cutset>
9
                                          <event>SC.G1.stuck_off_fault </event>
10
                                </\mathrm{cutset}>
11
                       </\mathrm{cutsets}>
12
             </transition>
13
             <transition>
14
                       <from_mode>mode = on_off_off </from_mode>
15
                      <to_mode>mode = off_off_on </to_mode>
16
                       <cutsets>
17
                                < cutset >
18
                                          <event>SC.GB1.stuck_open_fault
19
                                </\mathrm{cutset}>
20
                                <cutset>
21
                                          <event>SC.G1.stuck_off_fault</event>
22
23
                                </\mathrm{cutset}>
                       </\mathrm{cutsets}>
24
             </transition>
25
   </\mathrm{mtcs}>
26
```

Figure 7.8: XML output of MTCS found by command in Listing 7.6.

The result is as expected involving two failures, CC2 and a failure of G3 or GB3 in order to break power supply coming from G3.

7.3.10 Adding Fault Probability

A numerical probability can be associated to each fm and CC. From the grammar:

```
<fault-mode> ::=

MODE <mode-id> (probability-value)? ':'

<local-dynamics-model-id> <effect> ';'

<common-cause> ::=

CAUSE <id> (probability-value)?

(<cc-module-modes>)+

<probability-value> ::=

'{' <real-number> '}'

| '{' prob:<real-number> '}'
```

real-number can be a number $(0 \le N \le 1)$ like e.g.:

- 0.123
- 123.e-4, or 123.E-4 (IMPORTANT: notice the use of '.')
- 123.e-4, or 123.4e-5, or 0.001e+2, or 0.001e2

Precision is limited to 15 digits at the moment.

The value of probability can be given as a raw number, or it can be given as a named parameter called **prob**. The raw form is supported for backward compatibility, while the latter form is preferrable and should be used instead.

For example, prob:0.123e-3.

To associate a probability value to e.g. a Fault Mode of each extended instances of Generator:

```
1 MODE stuckAt_Off \{1.e-7\}: Permanent StuckAtByValue_D(...);
```

To associate a probability value to e.g. Common Cause CC1:

```
1 CAUSE CC1 {1.5e-8}
2 MODULE Generator
3 ...
```

When no probability is specified, 0 is assumed.

Remark: specifying that a fm has 0 probability does not disable the fm itself; the probability computed for the cut sets containing that fm will be set to be 0, but those cut sets will still appear in fault trees and FMEA tables.

When extending the model, the produced Fault modes xml file will contain probability information associated to each single module instance which is affected by the extension. E.g.

```
<?rxml version="1.0"?>
<compass>
<fmlist>
<fm name=".SC.G3.Gen_StuckOff.mode_is_stuckAt_Off" nominal_value="FALSE" probability="1.e-7"/>
<fm name=".SC.G2.Gen_StuckOff.mode_is_stuckAt_Off" nominal_value="FALSE" probability="1.e-7"/>
<fm name=".SC.G1.Gen_StuckOff.mode_is_stuckAt_Off" nominal_value="FALSE" probability="1.e-7"/>
<fm name=".asterCC._CC1.cc" nominal_value="FALSE" probability="1.5e-8"/>
</fmlist>
<obslist>
</compass>
```

The analysis will use this XML file as input, so changing probability values (on a per-instance basis) within it will propagate values to the analysis results.

7.3.11 Latent Faults

Being *latent* is a property of fault modes. A Fault Mode can be declared to be *possibly latent* (with an associated latent probability) with:

```
<fault-mode> ::=

MODE <mode-id> (probability-value-mode)? <colon>

<local-dynamics-model-id> <effect> <semi-colon>

<probability-value-mode> ::=

<lbra> <real-number> (, latent:yes|no, latent_prob:<real-number>)?<rbra>

| <lbra> prob:<real-number> (, latent:yes|no, latent_prob:<real-number>)?<rbra>
```

The latent-property is specified along with the Fault Mode probability. For example:

```
2 EXTENSION OF MODULE Generator
3 SLICE Gen_StuckOff AFFECTS state WITH
4
5 MODE stuckAt_Off {prob:1.e-7, latent:yes, latent_prob:1.e-6} :
6 Permanent StuckAtByValue_D(...);
```

In this example stuckAt_Off can be latent, and has a latent fault probability of 1.e-6. Notice that Common Causes cannot be declared to be latent.

7.4 TFPG Analysis

7.4.1 Associations file

A TFPG associations file has been created depending on the description contained at the beginning of Chapter 7. Here an extract of the complete example stored in file examples/fe/triple_modular_generator/tfpg/SC_TMG.axml is shown.

```
<associations>
<failureModes>
<failureModes>
</!-- generator: stuck off --->
<assoc id="G1_stuck_off" expr="SC.G1.Gen_StuckOff.mode_is_stuckAt_Off"/>
<!-- circuit breakers: stuck open --->
<assoc id="GB1_stuck_open"
        expr="SC.GB1.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Open"/>
<assoc id="BB1_stuck_open"
        expr="SC.BB1.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Open"/>
</assoc id="BB1_stuck_open"
</assoc id="SB1_stuck_open"</a>
```

1

3

4

5

6

7

8

```
<!-- circuit breakers: stuck closed --->
10
       <assoc id="GB1_stuck_closed"
               expr="SC.GB1.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Closed"/>
12
       <assoc id="BB1_stuck_closed"
               expr="SC.BB1.Switch_StuckClosed_StuckOpen.mode_is_stuckAt_Closed"/>
14
     </failureModes>
15
16
     <monitoredDiscrepancies>
       <!-- triple power generation: three generators are on --->
17
       <assoc id="TriplePowerUsage" expr="count(SC.G1.is_on, SC.G2.is_on, SC.G3.is_on) = 3"/>
18
19
     </monitoredDiscrepancies>
20
     <unmonitoredDiscrepancies>
       <assoc id="B1_broken" expr="SC.B1.is_broken"/>
21
22
       <assoc id="InconsPowerUsage"
               expr="count(SC.G1.is_on, SC.G2.is_on, SC.G3.is_on) > 0 &
23
24
                     count (SC.B1.is_powered, SC.B2.is_powered, SC.B3.is_powered) & lt; 3"/>
25
     </unmonitoredDiscrepancies>
     <tfpgModes>
26
27
       <assoc id="R1"
28
               expr="(SC.GB1.is_open & amp; SC.GB2.is_closed & amp;
                      SC.GB3.is_closed & amp; SC.BB1.is_open & amp;
29
30
                      SC.BB2.is_closed & amp; SC.BB3.is_open)"/>
     </tfpgModes>
31
32
   </associations>
```

For each Generator a failure mode stuck_off is created to represent the scenario in which a Generator breaks and propagates a failure through event fev_stuck_at_off. Additionally, two failure modes are added for each Circuit Breaker to depict the two admitted failure events, fev_stuck_at_closed and fev_stuck_at_open.

The discrepancy TriplePowerUsage is used to monitor the situation in which all the generators are powered on, while other discrepancies are included to check whether a bus is broken (e.g. B1_broken) or whether the power usage is inconsistent, i.e. some generator is working but not all buses are powered. These are some off-nominal conditions according to the system requirements described in Section 7.1.3.

Finally, the system mode R1 is used to denote a specific switching configuration of the circuit breakers.

7.4.2 Synthesis

The associations file described in the previous section can be used to synthesize a TFPG for the running example. To do that, the SMV model extended_SC_TMG.smv previously generated using the script extend_model.py needs to be used. The following command can be run for TFPG synthesis:

```
$> cd examples/fe/triple_modular_generator/tfpg
$> python ../../../scripts/synthesize_tfpg.py -a \
        SC_TMG.axml -m ../out/extended_SC_TMG.smv \
        -o SC_TMG_synthesized.txml --engine ic3 -b -B -c
```

For a complete description of script arguments see Section C.7.3. Depending on the model at hand, some engine settings might give better performance.

Result of synthesis

Here we show a possible result of a synthesis run. Any problems such as isolated failure mode nodes are reported at this output level.

```
tfpg synthesis > minimal cut set analysis
      -> Failure modes list dumped to out/extended_SC_TMG_sa_fm_list.xml
2
     ---> Extended model dumped to 'out/extended_SC_TMG_synth_extended.smv'
3
     discrepancy 1/2 (TriplePowerUsage)
4
     discrepancy 2/2 (InconsPowerUsage)
5
   tfpg synthesis > validating node names and cut sets
6
7
   tfpg synthesis > processing unreachable discrepancies
   tfpg synthesis > processing failure mode nodes
8
     failure mode 'GB1_stuck_open' has no effect on discrepancies within the analysis bound.
9
     failure mode 'GB2_stuck_open' has no effect on discrepancies within the analysis bound.
     failure mode 'GB3_stuck_open' has no effect on discrepancies within the analysis bound.
11
   tfpg synthesis > checking for independent discrepancies
   tfpg synthesis > checking for correlated discrepancies
13
   tfpg synthesis > creating causality graph
14
   tfpg synthesis > calling graph simplification routines
   tfpg synthesis > writing result to file
16
     tfpg file: SC_TMG_synthesized.txml
17
```

The generated TFPG can then be shown with the TFPG Viewer:

```
$> cd examples/fe/triple_modular_generator/tfpg
$> python ../../../scripts/view_tfpg.py -t SC_TMG_synthesized.txml
```

The TFPG Viewer opens and shows the TFPG:

The viewer offers the possibility of loading, saving and exporting a TFPG using the 'File' menu, while with the 'Edit' menu it is possible to edit the shown TFPG. In this case, an external editor with the textual representation of the TFPG is opened; to see the changes in the viewer the user must save and close the editor. Additionally, the toolbar allows to move, zoom and reset the shown TFPG.

7.4.3 Behavioral Validation

The synthesized TFPG can be validated using the validate_tfpg_behavior.py script. This can be done running the following command:

```
$> cd examples/fe/triple_modular_generator/tfpg
$> python ../../../scripts/validate_tfpg_behavior.py -a \
        SC_TMG.axml -m ../out/extended_SC_TMG.smv -k 20 -b \
        --tfpg-file SC_TMG_synthesized.txml
```

Result of behavioral validation

An example output for behavioral validation is shown as follows.

```
> extended smv model created (.../out/extended_SC_TMG_bv_extended_completeness.smv)
> 5 proof obligations generated (.../out/extended_SC_TMG_proof_obligations_completeness.txt)
> checking proof obligation 1/5 ... satisfied (within bound)
> checking proof obligation 2/5 ... satisfied (within bound)
> checking proof obligation 3/5 ... satisfied (within bound)
> checking proof obligation 4/5 ... satisfied (within bound)
> checking proof obligation 5/5 ... satisfied (within bound)
> checking proof obligation 5/5 ... satisfied (within bound)
> checking proof obligation 5/5 ... satisfied (within bound)
> checking proof obligation 5/5 ... satisfied (within bound)
```

1

2

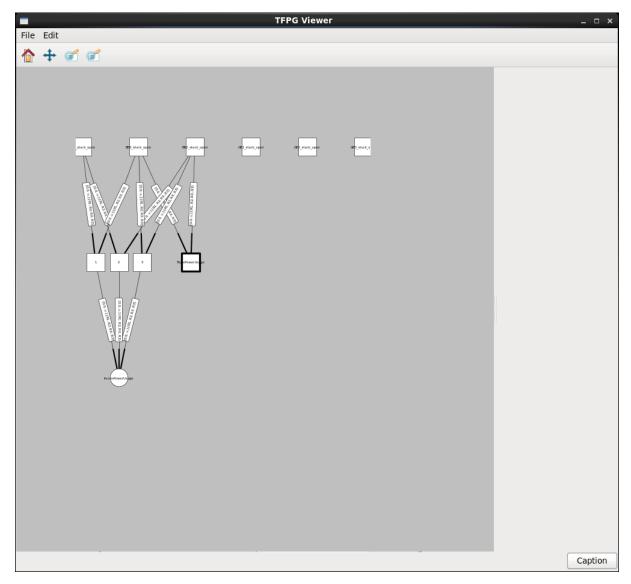


Figure 7.9: TFPG for file "SC_TMG_synthesized.txml"

7.4.4 Tightening

The synthesized TFPG can also be tightened using the tighten_tfpg.py script. This can be done running the following command:

\$> cd examples/fe/triple_modular_generator/tfpg
\$> python ../../../scripts/tighten_tfpg.py -a SC_TMG.axml \
 -t SC_TMG_synthesized.txml -m ../out/extended_SC_TMG.smv

Note that the TMG use case is untimed, thus only mode labels will be tightened.

Result of tightening

An example output for tightening is shown as follows. Some output has been omitted for brevity. The user is continuously informed how many parameters remain to be tightened.

TFPG Tightening

```
> skipping tightening of parameter 'tmin'
> skipping tightening of parameter 'tmax'
> initial completeness check
> given TFPG is complete
> tightening of modes
44 parameters remaining ..
43 parameters remaining .
42 parameters remaining .
[...]
15 parameters remaining ...
14 parameters remaining ...
13 parameters remaining ....
> instantiating tightened TFPG
Result: The TFPG has been tightened.
Output file: out/SC_TMG_synthesized_tight.txml
```

7.4.5 Statistics Information

The TFPG synthesized in the previous sections, can be used in order to retrieve some statistical information. Syntactical analysis can be performed by running:

```
1 $> cd example/fe/triple_module_generator/tfpg
2 $> python ../../../scripts/stats_tfpg.py SC_TMG_synthesized.txml -s
```

This produces the following output, showing the number and types of nodes, edges and modes contained in the TFPG:

```
All Nodes: 11
1
    FM Nodes: 6
2
   AND Nodes: 4
3
   OR Nodes: 1
4
5
    Monitored Nodes: 1
6
7
    Unmonitored Nodes: 4
8
9
    Edges: 11
10
    Modes: 4
11
```

modeb.

2 3

4 5

6

7 8

9

11

12

14

 $15 \\ 16$

17

18 19 20

21 22

23

7.4.6 Possibility, Necessity, Consistency and Activability

Possibility

Once we have synthesized our TFPG, we may want to check whether some traces are compatible or not with it.

The following trace says that, once the failure BB3_stuck_open is activated, as soon as the failure BB2_stuck_open is activated, so is TriplePowerUsage:

```
#BB3_stuck_open True
```

² BB3_stuck_open 0 3 #BB2_stuck_open True

⁴ BB2_stuck_open 1

^{5 #}TriplePowerUsage True

⁶ TriplePowerUsage 1

Indeed, running the following command:

```
1 $> cd example/fe/triple_module_generator/tfpg/smt
2 $> python ../../../../scripts/check_tfpg.py --open-infinity
3 --possibility --scenario scen_1.sc ../SC_TMG_synthesized.txml
```

we obtain a partial trace satisfying the scenario:

```
The scenario is possible!
   A model is:
2
   #intermediateNode_2 := True
3
   #intermediateNode_3 := False
4
   #intermediateNode_4 := False
5
   BB3_stuck_open := 0.0
6
   intermediateNode_4 := 3/2
7
   #InconsPowerUsage := True
8
   intermediateNode_2 := 1.0
g
   intermediateNode_3 := 0.0
10
   GB3_stuck_open := 0.0
11
   #TriplePowerUsage := True
   #BB1_stuck_open := False
13
   BB1_stuck_open := 2.0
14
   GB1_stuck_open := 0.0
15
   GB2\_stuck\_open := 0.0
16
   #GB2\_stuck\_open := False
17
18
   TriplePowerUsage := 1.0
   #BB3_stuck_open := True
19
   BB2\_stuck\_open := 1.0
20
21
   InconsPowerUsage := 1.0
   #BB2_stuck_open := True
22
   #GB3_stuck_open := False
23
   #GB1_stuck_open := False
24
```

On the other side, if we run the same check with the following scenario, which states that Triple_Power_Usage can be activated before than BB2_stuck_open, we get that the specified scenario is not possible.

#BB3_stuck_open True

- 2 BB3_stuck_open 0
- 3 #BB2_stuck_open True
- 4 BB2_stuck_open 1
- 5 #TriplePowerUsage True
- 6 TriplePowerUsage 0

Necessity

Necessity is useful in case we want to check whether a particular scenario is implied by our TFPG; suppose to have the following simple scenario:

#TriplePowerUsage True

We are saying that the discrepancy Triple_Power_Usage is activated (no matter when); we can use the necessity check to verify that this is not implied by our TFPG, i.e. that the discrepancy is not always activated.

```
1 $> cd example/fe/triple_module_generator/tfpg/smt
2 $> python ../../../scripts/check_tfpg.py --open-infinity
3 --necessity --scenario scen_2.sc ../SC_TMG_synthesized.txml
```

As expected, this is not the case:

```
The scenario is NOT necessary!
```

```
2 A counterexample scenario is:
```

```
3 #intermediateNode_2 := False
```

```
4 \#intermediateNode_3 := False
```

$ \begin{array}{llllllllllllllllllllllllllllllllllll$	5	$#intermediateNode_4 := False$
<pre>8 #InconsPowerUsage := False 9 intermediateNode_2 := -1.0 10 intermediateNode_3 := -1.0 11 GB3_stuck_open := 0.0 12 #TriplePowerUsage := False 13 #BB1_stuck_open := False 14 BB1_stuck_open := 0.0 15 GB1_stuck_open := 0.0 16 GB2_stuck_open := 0.0 17 #GB2_stuck_open := False 18 TriplePowerUsage := 1.0 19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False</pre>	6	$BB3_stuck_open := 0.0$
9 intermediateNode_2 := -1.0 10 intermediateNode_3 := -1.0 11 GB3_stuck_open := 0.0 12 #TriplePowerUsage := False 13 #B1_stuck_open := 0.0 15 GB1_stuck_open := 0.0 16 GB2_stuck_open := 0.0 17 #GB2_stuck_open := 1.0 19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False	7	$intermediateNode_4 := -1.0$
<pre>10 intermediateNode_3 := -1.0 11 GB3_stuck_open := 0.0 12 #TriplePowerUsage := False 13 #BB1_stuck_open := False 14 BB1_stuck_open := 0.0 15 GB1_stuck_open := 0.0 16 GB2_stuck_open := 0.0 17 #GB2_stuck_open := False 18 TriplePowerUsage := 1.0 19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False</pre>	8	#InconsPowerUsage := False
<pre>11 GB3_stuck_open := 0.0 12 #TriplePowerUsage := False 13 #BB1_stuck_open := False 14 BB1_stuck_open := 0.0 15 GB1_stuck_open := 0.0 16 GB2_stuck_open := 0.0 17 #GB2_stuck_open := False 18 TriplePowerUsage := 1.0 19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False</pre>	9	$intermediateNode_2 := -1.0$
<pre>12 #TriplePowerUsage := False 13 #BB1_stuck_open := False 14 BB1_stuck_open := 0.0 15 GB1_stuck_open := 0.0 16 GB2_stuck_open := 0.0 17 #GB2_stuck_open := False 18 TriplePowerUsage := 1.0 19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False</pre>	10	$intermediateNode_3 := -1.0$
<pre>13 #BB1_stuck_open := False 14 BB1_stuck_open := 0.0 15 GB1_stuck_open := 0.0 16 GB2_stuck_open := 0.0 17 #GB2_stuck_open := False 18 TriplePowerUsage := 1.0 19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False</pre>	11	$GB3_stuck_open := 0.0$
<pre>14 BB1_stuck_open := 0.0 15 GB1_stuck_open := 0.0 16 GB2_stuck_open := 0.0 17 #GB2_stuck_open := False 18 TriplePowerUsage := 1.0 19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False</pre>	12	#TriplePowerUsage := False
<pre>15 GB1_stuck_open := 0.0 16 GB2_stuck_open := 0.0 17 #GB2_stuck_open := False 18 TriplePowerUsage := 1.0 19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False</pre>	13	$#BB1_stuck_open := False$
<pre>16 GB2_stuck_open := 0.0 17 #GB2_stuck_open := False 18 TriplePowerUsage := 1.0 19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False</pre>	14	$BB1_stuck_open := 0.0$
<pre>17 #GB2_stuck_open := False 18 TriplePowerUsage := 1.0 19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False</pre>	15	$GB1_stuck_open := 0.0$
<pre>18 TriplePowerUsage := 1.0 19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False</pre>	16	$GB2_stuck_open := 0.0$
<pre>19 #BB3_stuck_open := False 20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False</pre>	17	$#GB2_stuck_open := False$
20 BB2_stuck_open := 0.0 21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False	18	TriplePowerUsage := 1.0
21 InconsPowerUsage := -2.0 22 #BB2_stuck_open := False 23 #GB3_stuck_open := False	19	#BB3_stuck_open := False
22 #BB2_stuck_open := False 23 #GB3_stuck_open := False	20	$BB2_stuck_open := 0.0$
23 #GB3_stuck_open := False	21	InconsPowerUsage := -2.0
	22	$\#BB2_stuck_open := False$
24 #GB1_stuck_open := False	23	$#GB3_stuck_open := False$
	24	$#GB1_stuck_open := False$

Consistency

We can check whether our TFPG is consistent (i.e. there is at least a complete trace for it) by running the following command:

```
$> cd example/fe/triple_module_generator/tfpg/smt
   $> python ../../../../scripts/check_tfpg.py --open-infinity
--consistency ../SC_TMG_synthesized.txml
2
3
4
```

```
$> The TFPG is consistent!
```

Activability

At last, we can check that all the nodes of our TFPG can be activated; this can be verified by executing the following command:

```
$> cd example/fe/triple_module_generator/tfpg/smt
$> python ../../../../scripts/check_tfpg.py --open-infinity
--activability ../SC_TMG_synthesized.txml
$> Checking if all nodes can be activated ...
All nodes can be activated!
```

7.4.7Diagnosis

Consider the following scenario, in which our observations refer to the failures BB2, BB3, TriplePowerUsage and to the mode R3.

```
#BB3_stuck_open True
  #BB2_stuck_open True
2
  #TriplePowerUsage True
3
  #mode R3
```

4

1 2

3

2 3

4

We may want to enumerate all possible sets of failure modes compatible with the observations; this can be done using the following command:

```
$> cd example/fe/triple_module_generator/tfpg/smt
$> python ../../../../scripts/compute_tfpg_diagnosis.py --open-infinity
     --diagnosability -d scen_3.sc ../SC_TMG_synthesized.txml
```

As a result, we obtain all the possible combinations of the remaining failures (BB1, GB1, GB2 and GB3).

```
Getting all the diagnoses ...
2
   16 diagnoses found.
3
   #BB3_stuck_open := True
4
   \#mode := R3
   #GB3_stuck_open := False
6
   #GB2_stuck_open := False
7
   #GB1_stuck_open := False
8
   #BB2_stuck_open := True
9
   #BB1_stuck_open := False
11
   \#BB3\_stuck\_open := True
   \#mode := R3
13
   #GB3_stuck_open := False
14
   #GB2_stuck_open := False
   #GB1_stuck_open := True
16
17
   #BB2_stuck_open := True
   #BB1_stuck_open := False
18
19
20
    . . .
```

This corresponds to what we would expect, because in our synthesized TFPG the mode R3 is enabled in all edges.

If we manually edit our TFPG by removing R3 from the modes in the edges having destination TriplePowerUsage, we will se that no diagnosis is found, as in this case it is impossible to activate TriplePowerUsage remaining in mode R3.

7.4.8 Refinement

Suppose now that we want to restrict the set of possible behaviors of our synthesized TFPG. We can create another TFPG (see:

file examples/fe/triple_modular_generator/tfpg/smt/SC_TMG_refined.tfpg) and check whether it is a refinement of the original one.

As we can see, the new TFPG is built on top of the synthesized one, with the following changes:

- intermediate nodes have been renamed
- max time for the InconsPowerUsage has been set to 1.0
- modes over edges have been changed

We can verify that this new tfpg is a refinement of the original synthesized one by running the following command:

```
$> cd example/fe/triple_module_generator/tfpg/smt
$> python ../../../../scripts/check_tfpg_refinement.py -m mapping.txt
    -r ../SC_TMG_synthesized.txml --open-infinity SC_TMG_refined.tfpg
$> Checking the refinement...
The given TFPG is a refinement of original_tfpg
```

7.4.9 Filtering

1

2

3

4

Whereas refinement validates a manual manipulation of a given TFPG, xSAP also provides automatic means to manipulate TFPGs. In the running example, for instance, we might be interested only in the paths leading to the discrepancy *TriplePowerUsage*. A TFPG containing only those paths can be created as follows:

The command will give the following output:

2

3 4 5

6 7

2

3

```
TFPG Filtering > computing reachability set

TFPG Filtering > removing nodes from which focus nodes cannot be reached

TFPG Filtering > dumping result

The filtered TPFG file has been saved at 'out/SC_TMG_synthesized_focused.txml'
```

The resulting TFPG contains only the failure mode nodes *B2_stuck_open* and *BB3_stuck_open*, as well as the discrepancy *TriplePowerUsage*.

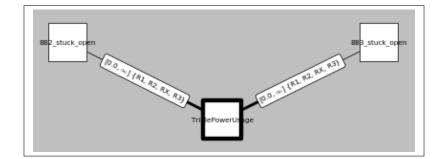


Figure 7.10: TFPG restricted to paths leading to TriplePowerUsage.

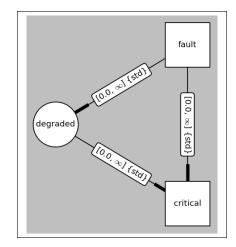


Figure 7.11: TFPG with a redundant edge.

Similarly, the script can be used to remove redundant edges on given TFPGs. For instance, consider the TFPG in Figure 7.11.

The edge from *fault* to *critical* is redundant. Invoking the script as follows will produce a new TFPG, where the redundant edge has been removed:

As the simplification routines inside xSAP assume maximally permissive edges, this simplification functionality will only be executed if all edges have $t_{min} = 0$, $t_{max} = +\infty$ and the modes set to all modes known to the TFPG.

7.5 Fault Detection and Isolation

Fault Detection and Isolation (FDI) analysis is performed using the extended version of the Triple Module Generator model. In this model, the FDI model has been removed, as our goal is to synthesize it automatically from an FDI specification. The modified version can be found under file examples/FDI/extended_SC_TMG_empty_controller.smv.

In the next sections, we exemplify the different analyses separately.

7.5.1 Diagnosability analysis

As an example, we try to detect the generic fault for the generator G1. We expect that, knowing the received command and the state of G1, we should be able to successfully detect it. Hence, the observables file can be defined as follows:

```
SC.G1.state
CN.cmd_G1
```

The specific condition we want to diagnose is SC.G1.Gen_StuckOff.mode != NOMINAL, i.e. that G1 is failed at "StuckOff".

To run diagnosability, we can use the Python script provided by xSAP:

```
2
```

The check fails, and provides a pair of traces as counterexample; the fault occurs on the first trace but never on the second one, and yet both traces produce the same observations. We can see on the traces that after the fault the generator is never sent the command cmd_on, thus by observing only its state it is impossible to say whether the fault occurred or not. The traces can be rendered graphically using the script *view_trace.py*, as shown in appendix C.8.1.

We could however make an assumption on the environment or external controller that is not included in our model, and say that this command is sent periodically within a bound of 5 time units. Under this context restriction, the problem is indeed diagnosable within a delay bound of 5 time units.

In the previous example invoking the diagnosability script we specified the alarm condition directly on the command line. For the check with the context we now show how a specification file can be used instead (see Figure 7.12).

```
NAME: alarm_G1
CONDITION: SC.G1.Gen_StuckOff.mode != NOMINAL
```

```
3 TYPE: finite
```

4

```
CONTEXT: G F [0,5] CN. cmd_G1 = cmd_on
```

Figure 7.12: ASL file for diagnosability check with context.

Dignosability is then launched as follows.

\$> python ../../scripts/check_diagnosability.py -m ../extended_SC_TMG_empty_controller.smv -o G1_observables.obs --asl-file G1_with_context.asl

7.5.2 Minimum observables set analysis

We now want to check whether the observables we previously used are the minimum set needed to detect the fault of the generator G1; moreover, we want to check whether it is the only configuration which allows to detect it, or alternative configurations are possible. We increase for this analysis the set of observables that should be considered:

SC.G1.state CN.cmd_G1 2 SC.G2.state 3 CN.cmd_G2 4 SC.G3.state 5 $CN.cmd_G3$ 6 SC.GB1.state 7 CN.cmd_GB1 8 9 SC.GB2.state CN cmd GB2 10 SC.GB3.state $CN.\,cmd_GB3$ 12 13 SC.BB1.state CN.cmd_BB1 14 SC.BB2.state 15 $CN. cmd_BB2$ 16 SC.BB3.state 17CN. cmd_BB3 18 SC.B1_poweredby_G3_R 19SC.B1_poweredby_G3_L 20 $SC.B1_poweredby_G2_R$ 21 $SC.B1_poweredby_G2_L$ 22 SC.B1_poweredby_G1_U 23 $SC.B2_poweredby_G3_R$ 24 SC.B2_poweredby_G3_L 25SC.B2_poweredby_G2_U 2627 $SC.B2_poweredby_G1_R$ SC.B2_poweredby_G1_L 28 29 SC.B3_poweredby_G3_U 30 SC.B3_poweredby_G2_R SC.B3_poweredby_G2_L 31 32 $SC.B3_poweredby_G1_R$ SC.B3_poweredby_G1_L 33 SC.B1_poweredby_G3 34 SC.B1_poweredby_G2 35 36 SC.B1_poweredby_G1 $SC.B2_poweredby_G3$ 37 $SC.B2_powered by_G2$ 38 SC.B2_poweredby_G1 39 $SC.B3_powered by_G3$ 40 SC.B3_poweredby_G2 41 SC.B3_poweredby_G1 42 43 SC.B1.stateSC.B2.state44SC.B3.state 45

2

To run minimum observables set generation, we can run the following script provided by xSAP. The arguments are the same as for diagnosability checking, but now instead of verifying the observables we want to optimize them.

```
1 $> cd examples/FDI/diag
```

2 3

4

```
-x "G F [0,5] CN.cmd_G1 = cmd_on" -o full_observables.obs
```

The result consists of the following set:

```
1) ------
> SC.G1.state
cost: 1
```

Note that the minimization procedure concludes that the command signal doesn't need to be observed, by assumption of context. The fault can thus be diagnosed by observing the state of generator G1, and triggering the alarm if it is off for more than 5 time units.

7.5.3 Synthesis of a diagnoser

A diagnoser for the generator G1 can be generated running the following xSAP commands. First we specify the definition of the observables and the alarms (associated with the fault we want to diagnose), and then we run the synthesis and write the generated model. The alarm specification file contains the following:

```
NAME: alarm_G1
CONDITION: SC.G1.Gen_StuckOff.mode != NOMINAL
TYPE: finite
CONTEXT: G F (CN.cmd_G1 = cmd_on)
```

In this particular case, we can omit the context and obtain the same diagnoser. However, the validation properties will show us that without context, we cannot always raise the alarm.

```
2
```

2

3

```
$> cd examples/FDI/synth
$> python ../../.scripts/synthesize_fd.py -m ../extended_SC_TMG_empty_controller.smv \
    -o G1_observables.obs -f G1.asl --out-file G1_synthesized_model.smv
```

where $G1_{observables.obs}$ contains the observables of the system, and G1.asl contains the alarm specification.

The generated model, called *G1_synthesized_model.smv*, consists of the original model combined with the synthesized FDI module. An excerpt of the model is presented here:

```
1MODULE __FD("CN.cmd_G1", "SC.G1.state")
     VAR
2
3
         __state : 1 .. 13;
4
     DEFINE
5
         Ualarm_G1 := (( __state = 4 | __state = 7) | __state = 11);
6
7
         Knalarm_G1 := ((( \_state = 2 | \_state = 9) | \_state = 5)
                                                                          __state = 1);
         Kalarm_G1 := (((( __state = 8 | __state = 3) | __state = 10) | __state = 12) |
8
                           \_\_state = 6);
9
```

The parameters of the $_FD$ module are the observables we previously specified. The variable *state* is used to take into account all the possible states of the diagnoser; the system evolves depending on the possible combinations of the values of the observables.

Three DEFINE ($Ualarm_G1$, $Knalarm_G1$ and $Kalarm_G1$) statements are added (notice that $alarm_G1$ is the name we specified for the alarm) that define the states in which the fault expression is satisfied ($Kalarm_G1$), it is not satisfied ($Knalarm_G1$) or it is unknown ($Ualarm_G1$).

7.5.4 Effectiveness analysis

To validate the previous diagnoser $(G1_synthesized_model.smv)$ we can model check the following three properties, that are automatically generated by the synthesis routines:

- 1. LTLSPEC G (__myfdir.myfd.Knalarm_G1 ->
 !(0 SC.G1.Gen_StuckOff.mode != NOMINAL))
- 3. LTLSPEC (G (SC.G1.Gen_StuckOff.mode != NOMINAL ->
 F __myfdir.myfd.Kalarm_G1))

The first and second properties are *correctness* properties, and they should be valid for any synthesized diagnoser. The first property states that it is never the case that the fault occurred in the past, if the diagnoser knows for sure that it did not. The second property states that if the diagnoser knows for sure that the fault occurred, then it did indeed occur in the past. As expected, both properties hold.

The third property expresses *completeness* and holds if the given fault is diagnosable, not considering context. It encodes the fact that, if the system is in a state in which the mode is not nominal, eventually the corresponding fault alarm will be raised. This property doesn't hold in the model, since other behaviors outside the context are possible. However, when adding the context to the property, the proof obligation holds, indeed also within the time bound 5:

• (G F [0,5] CN.cmd_G1 = cmd_on) -> G (SC.G1.Gen_StuckOff.mode != NOMINAL -> F [0,5] __myfdir.myfd.Kalarm_G1)

Chapter 8

Conclusions and Future Directions

xSAP is a system that supports a formal, Model-Based Safety Assessment with along two main directions: *symbolic fault extension*, that allows the user to automatically obtain an model including the faulty behaviours from a source, nominal model; and procedures for *safety analysis*, such as FTA and FMEA, that allow the user to analyze the system under fault.

In the subsequent releases, the following extensions to xSAP will be considered. First, it will be instrumented to deal with asynchronous composition, and with continuous-time models [17]. Second, xSAP be extended to deal with fault-extension for a contract-based design flow, as described in [14]. Finally, xSAP will integrate capabilities for the analysis of reliability architectures [12, 13].

An extension to the probabilistic setting, as supported by the COMPASS toolset [11], is currently under consideration.

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Appendix A

Installation

This chapter describes the necessary hardware/software configuration needed to run the xSAP tool and how to stay up to date with the latest updates.

A.1 Prerequisites

Since both Windows (64 bit and 32 bit) and Linux (64 bit) platforms are supported, and since Windows and Linux have very different packaging and installation procedures, and come with a largely different base of software, prerequisites are described separately in this section, for Linux and Windows systems.

Important: In directory scripts there is file check_installation.py which runs a set of tests to check installation requirments. Run it when done with the installation for a sanity check.

A.1.1 Platform-independent

SDEs All SDEs are Eclipse components which can be updated from this site: https://es-static.fbk.eu/tools/devel_sde/

Requisite: Eclipse Neon (https://projects.eclipse.org/releases/neon)

A.1.2 Microsoft Windows (64 bit and 32 bit)

nuXmv, ocra and xSAP OCRA, NUXMV and xSAP executables are statically linked, or when needed are shipped along with libraries they need.ma

However, a preprocessors like "cpp" and/or "m4" should be installed as separate packages. For "cpp", see for example GNU CPP at https://gcc.gnu.org/. For a binary executable see e.g. http://tdm-gcc.tdragon.net/download and select the version corresponding to the desired architecture (64 or 32 bit).

Scripts Scripts are all located in top-level directory file scripts.

To execute them, a Python 2.7.x interpreter is needed. It can be downloaded from here: https://www.python.org/ftp/python/2.7.8/python-2.7.8.amd64.msi

Viewers All requisites for running scripts, plus:

lxml Install selecting 64-bit package for the correct python version from http://www.lfd. uci.edu/~gohlke/pythonlibs/#lxml

gtk Version 2.22.1

- Download bundle from here: http://ftp.gnome.org/pub/gnome/binaries/win64/ gtk+/2.22/gtk+-bundle_2.22.1-20101229_win64.zip
- 2. Unpack e.g. in c:\opt\gtk-2.22.1
- 3. Put c:\opt\gtk-2.22.1\bin in system path
- 4. In a shell, try running gtk-demo to see if installation is OK

pygobject, py2cairo, pygtk Install selecting (64-bit or 32-bit) packages from http://www. lfd.uci.edu/~gohlke/pythonlibs/#pygtk and http://www.lfd.uci.edu/~gohlke/ pythonlibs/#pycairo

dateutil Version 2.2

Install selecting (64-bit or 32-bit) package for the correct python version from http: //www.lfd.uci.edu/~gohlke/pythonlibs/#python-dateutil

numpy Version 1.8.1

Install selecting (64-bit or 32-bit) package for the correct python version from http: //www.lfd.uci.edu/~gohlke/pythonlibs/#numpy

pyparsing Version 2.0.2

Install selecting (64-bit or 32-bit) package for the correct python version from http: //www.lfd.uci.edu/~gohlke/pythonlibs/#pyparsing

matplotlib Version 1.3.1

Install selecting (64-bit or 32-bit) package from http://www.lfd.uci.edu/~gohlke/ pythonlibs/#matplotlib

networkx Version 1.8.1 or later Install selecting (64-bit or 32-bit) package from http://www. lfd.uci.edu/~gohlke/pythonlibs/#networkx

pygraphviz Version 1.2 or later

- Install graphviz-2.xx.msi (version 2.32 or later) from http://www.graphviz.org/ download/
- Install selecting (64-bit or 32-bit) package from: http://www.lfd.uci.edu/~gohlke/ pythonlibs/#pygraphviz

A.1.3 Linux 64 bit

nuXmv, ocra and xSAP OCRA, NUXMV and xSAP executables are statically linked.

Scripts Scripts are all located in top-level directory file scripts.

To execute them, a Python 2.7.x interpreter is needed. The Python interpreter should be already installed by default on any Linux distribution.

Should version 2.7.x be not installed, install the corresponding package (requires generally root priviledges).

Viewers All requisites for running scripts, plus:

lxml Version 3.2.3 or later. Install the corresponding package, or use pip.

gtk Version 2.22.1 or later, but stick with version 2.x. Install the corresponding package.

pygobject, py2cairo, pygtk Install corresponding packages.

matplotlib Version 1.3.1 or later

Install corresponding package.

- **networkx** Version 1.8.1 or later Install corresponding package.
- **graphviz** Version 2.32 or later Install corresponding package.
- **pygraphviz** Version 1.2 or later Install corresponding package.
- goocanvas and pygoocanvas Version 1.x Install corresponding packages.

Appendix B

Syntax Directed Editor

Syntactic Errors and Auto-completion The model viewer is able to detect and underline syntactic and semantic errors in the model and they can be viewed both in the related section of the code and in the "*Output Console*" which maintains also a history description. Another important feature of the model viewer is the auto-completion which aid the modeling by suggesting a set of reasonably possible keywords/words.

Appendix C

Script Guide

This section provides a reference for the scripts provided by xSAP.

C.1 Model Extender

The Model Extender takes as inputs:

- A nominal SMV model
- A Fault Extension Instruction file (FEI)

The Model Extender produces as output:

- A SMV model extended with fault behaviours
- A list of fault modes as XML file

Both outputs will be used in the Safety Analysis, while the extended SMV model can be used for model-checking. If the provided input contains errors, logging files will be produced.

The Model Extender is a combination of tools which process the input and perform the actual extension. The following script is available for this task.

```
python ../../scripts/extend_model.py -h
usage: extend_model.py [-h] [--xml-fei] [--verbose] [--disable-checks]
[-p PATH] [-d PATH] [--disable-cc] [--anonymize]
SMV-FILE FEI-FILE
Produces an extend smv file out of given nominal smv file and fei
positional arguments:
                                            The input nominal smv file name
   SMV-FILE
   FEI-FILE
                                           The input fei file name
optional arguments:
   -h, --help
--xml-fei, -x
--verbose, -v
                                           show this help message and exit
   -n, --neip show this help message and exit

--xml-fei, -x Process XML-format for input fei file

--verbose, -v Sets verbosity to high level

--disable-checks, -c Disable semantics checks when extending model (for

debugging)
   -p PATH, --path PATH Path to the extension library (default:/hardmnt/mason0
/sra/bozzano/SMV/ESTools/xSAP/data/fm_library)
-d PATH, --outdir PATH
                                            Output directory, where all generated file should be
put into (default:out)
Disable generation of common cause encoding when
     -- disable -cc, -C
                                            extending model (for debugging)
Anonymize the generated extended model
   --- anonymize , -- A
```

All options are not mandatory, it can be run simply with:

```
$> python scripts/extend_model.py <nominal-model.smv> <fei.txt>
```

Additionally, option -v enables verbose messages to the user.

C.2 Fault Tree Analysis

Fault Tree analysis requires:

- A SMV model extended with fault behaviours
- A list of fault modes as XML file

It produces as output:

- A file containing the events of the generated fault tree
- A file containing the gates of the generated fault tree

If errors are encountered, logging files will be provided. The following script is available to call the FTA procedures.

```
python ../../scripts/compute_ft.py -h
usage: compute_ft.py [-h] [--smv-file SMV-FILE] [--fms-file FMS-FILE]
[--faults-bound BOUND] [--prop-index INDEX]
[--prop-name NAME] [--prop-text PROPERTY] [--verbose]
[--engine {bdd,bmc,bddbmc,msat,ic3,bmc_ic3}] [--dynamic]
[-gen-trace] [--bmc-length BMC_LENGTH] [--show]
[--probability] [--symbolic] [--quick] [--intermediate]
                                               -d PATH
                                                 -boolean-conversion-uses-predicate-normalization]
Produces an extend smv file out of given nominal smv file and fei
 optional arguments:
    -h, --help
--smv-file SMV-FILE
--fms-file FMS-FILE
                                                    show this help message and exit
                                                   The input extended smv file name
The input fault mode xml file name
    Sets a bound to the maximum number of faults
--prop-index INDEX, --n INDEX
                                                    Property index to be used as TLE
    --prop-name NAME, -P NAME
                                                    Property name to be used as TLE
    --prop-text PROPERTY, -p PROPERTY
Textual property to be used as TLE
    --verbose, -v Sets verbosity to high level

--engine {bdd,bmc,bddbmc,msat,ic3,bmc_ic3}, -E {bdd,bmc,bddbmc,msat,ic3,bmc_ic3}

Use given engine (default: bdd)

--dynamic Generates dynamic fault tree

    --dynamic Generates dynamic fault tree
    --gen-trace, -e Generates xml trace from fault tree
    --bmc-length BMCLENGTH, -k BMCLENGTH
    Specify BMC length (integer)
    --show, -s Show the generated fault tree
    --probability Computes probability when generating FT
    --quick, -Q For quick computation, avoid ordering the FT and when computing probability avoid computation.
    --intermediate, -I Generates intermediate FTs for each layer

    -d PATH, --outdir PATH
       Output directory, where all generated file should be
put into (default:out)
--boolean-conversion-uses-predicate-normalization, -b
                                                    Enables predicate normalization during boolean conversion
```

No option is required; if no smv model and fault modes files are provided, the script automatically looks for them in the ./out directory. Files are selected only if there is no possible ambiguity, and choices are always reported by verbose messages. Additionally, option -v enables verbose messages to the user.

C.3 FMEA Table Analysis

FMEA Table analysis requires:

- A SMV model extended with fault behaviours
- A list of fault modes as XML file

It produces a textual file and an xml file containing the generated FMEA table. If errors are encountered, logging files will be provided. FMEA can be performed using the following script.

<pre>python//scripts/co usage: compute_fmea_tab</pre>	mpute_fmea_table.py
0	[prop-indices INDICES][prop-names PROP-NAMES]
	[props-text PROPERTIES] [verbose]
	[engine {bdd, bmc, msat }] [dynamic] [compact]
	[gen-trace] [card CARD]
	[bmc-length BMCLENGTH] [show] [-d PATH]
Produces an extend smv	file out of given nominal smv file and fei
optional arguments:	
-h, $help$	show this help message and exit
smv-file SMV-FILE	The input extended smv file name
fms-file FMS-FILE	The input fault mode xml file name
prop-indices INDICE	
	Property indices to be used as TLE (separated by ':',
	',', or spaces, ranges like $'0-10'$ are allowed)
prop-names PROP-NAM	
	Property names to be used as TLE (separated by ':', or
	',')
props-text PROPERTY	
	Textual property to be used as TLE(separated by ':' or
	· , ·)
verbose, -v	Sets verbosity to high level
engine {bdd,bmc,msa	
	Use given engine (default: bdd)
dynamic	Generates dynamic fmea table
compact, -c	Generates compact fmea table
gen-trace, -e	Generates xml trace from fmea table
	Cut-Set cardinality (default: 1)
bmc-length BMC_LENG	
	Specify BMC length (integer)
	Show the generated fmea table
-d PATH,outdir PAT	
	Output directory, where all generated file should be
	put into (default:out)

No option is required; if no SMV model and fault modes files are provided, the script automatically looks for them in the ./out directory. Files are selected only if there is no possible ambiguity, and choices are always reported by verbose messages. Additionally, option -v enables verbose messages to the user.

C.4 MTCS Analysis

MTCS analysis requires:

- A SMV model extended with fault behaviours
- A list of events as XML file
- A list of expressions as command arguments

It produces as output:

- A file containing MTCS in XML format
- Optionally, a file containing MTCS in dot or tex format

```
usage: compute_mtcs.py [-h] [--smv-file SMV-FILE] [--fms-file FMS-FILE]
[--verbose] [--use-vars] [--paging]
[--visual-out FORMAT] [--layering] [--single-source]
--expressions ... [-d PATH]
[--boolean-conversion-uses-predicate-normalization]
Computes MTCS for given modes, smv file and fms file
optional arguments:
-h, --help show this help message and exit
--smv-file SMV-FILE The input extended smv file name
--fms-file FMS-FILE The input fault mode xml file name
--fms-file FMS-FILE The input fault mode xml file name
--verbose, -v Sets verbosity to high level
--use-vars, -V Treats expressions as state mode variables
--paging, -g In visual output, print each transitions separately
--visual-out FORMAT, -o FORMAT
Visual output format, either tex or dot
--layering, -L Turn off layering
--expressions ... List of expressions defining mode variables or modes
-d PATH, --outdir PATH
Output directory, where all generated file should be
put into (default:out)
--boolean-conversion-uses-predicate-normalization, -b
Enables predicate normalization during boolean
conversion
```

The only required option is the list of expressions. If no smv model and fault modes files are provided, the script automatically looks for them in the ./out directory. Files are selected only if there is no possible ambiguity, and choices are always reported by verbose messages. Additionally, option -v enables verbose messages to the user.

C.5 Diagnosability

C.5.1 Diagnosability Analysis

Diagnosability analysis requires:

- An SMV model (extended with fault behaviours)
- A condition that needs to be diagnosed

The analysis answers positively if the condition is diagnosable, and provides a counterexample otherwise. If errors are encountered, logging files will be provided. Diagnosability analysis can be performed using the following script.

```
python scripts/check_diagnosability.py -h
usage: check_diagnosability.py [-h] [--engine {bdd,bmc,msat_bmc,ic3}]
-bmc-length BMCLEN] [--smv-file SMV]
[--diagnosis -condition DIAG_COND]
[--alarm-pattern {exact,bounded,finite}]
[--delay-bound DELAYBOUND]
[--context-expression LTL_CONTEXT]
[--observables-file OBS_FILE]
[--asl-file ASL_FILE]
[--asl-file ASL_FILE]
[--werbosity-level VERBOSE_LEVEL]
optional arguments:
-h, --help show this help message and exit
--engine {bdd,bmc,msat_bmc,ic3}, -E {bdd,bmc,msat_bmc,ic3}
Use given engine (default: ic3)
--bmc-length BMCLEN, -k BMCLEN
Maximum path length for BMC
--smv-file SMV, -m SMV
SMV file
--diagnosis-condition DIAG_COND, -c DIAG_COND
Diagnosis condition
--alarm-pattern {exact,bounded,finite}
Alarm pattern (default: None)
--delay-bound DELAYBOUND, -d DELAYBOUND
Alarm pattern delay bound
--context-expression LTL_CONTEXT, -x LTL_CONTEXT
LTL context expression
--observables-file OBS_FILE
File specifying observable variables
--asl-file ASL_FILE, -f ASL_FILE
```

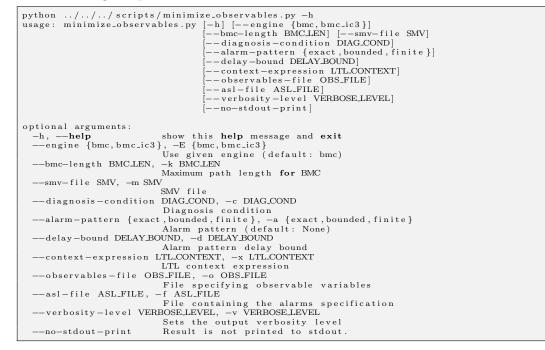
File containing the alarms specification --verbosity-level VERBOSE_LEVEL, -v VERBOSE_LEVEL Sets the output verbosity level

C.5.2 Generation of Minimum Observables Set

Generation of minimum observables requires the same inputs as diagnosability analysis:

- An SMV model (extended with fault behaviours)
- A condition that needs to be diagnosed

The minimal sets of observables under which the condition is diagnosable will be returned. If errors are encountered, logging files will be provided. The analysis can be performed using the following script.



C.6 FD Synthesis

FD synthesis requires:

- An SMV model (extended with fault behaviours)
- A set of observables variables
- An ASL specification

This provides in output an FD for the system that satisfies the ASL specification. If errors are encountered, logging files will be provided. FD Synthesis can be performed using the following script.

usage: synthesize_fd.py [-h] [smv-file SMV] [observables-file OBS_FILE] [asl-file ASL_FILE] [stand-alone] [out-file TARGET_SMV] [-verbosity-level VERBOSE_LEVEL] [composition-semantics COMPOSITION_SEMANTICS] [no-dag]	
optional arguments:	
-h,help show this help message and exit	
smv-file SMV, -m SMV	
SMV file	
observables-file OBS_FILE, -o OBS_FILE	
File specifying observable variables asl-file ASL_FILE, -f ASL_FILE	
File specifying the alarm specification	
stand-alone Output only the FD (By default outputs the combination of FD and plant)	
out-file TARGET_SMV	
Where to write the synthesized model	
verbosity-level VERBOSE_LEVEL, -v VERBOSE_LEVEL	
Sets the output verbosity level composition-semantics COMPOSITION.SEMANTICS	
Specify composition semantics:(s)ynchronous or (a)synchronous Do not use the DAG printing of the transition relation(This should be used only on small FD)	

C.7 TFPG

C.7.1 Format Conversion

xSAP supports two formats for TFPGs: xml and textual. It is possible to switch between the two formats using the following script.

It is possible to convert both a TFPG or an associations file; the specific option is required.

C.7.2 TFPG Generation

TFPGs in xml format can be generated automatically. No options are required; if no filename is specified, a TFPG named out.txml is created.

C.7.3 TFPG Synthesis

TFPG synthesis is carried out specifying a SMV model extended with fault behaviours and an associations file. The synthesized TFPG is created in the chosen output directory. If no name is specified, the suffix _synth_tfpg.txml is added to the name of the SMV model. If errors are encountered, logging files will be provided. The following script can be used for this task.

python//scripts/synthesize_tfpg.py -h usage: synthesize_tfpg.py [-h] [associations-file ASSOC] [smv-file SMV] [output-tfpg OUT_TFPGFNAME] [engine {bmc,bdd,msat_bmc,ic3,bmc_bdd,sbmc_bdd}] [force-boolean-ic3] [bmc-length BMC_LEN] [dynamic] [biolean-conversion-uses-predicate-normalization] [disable-coi-reduction] [prune-unreachable-nodes]	
[verbosity-level VERBOSE_LEVEL] [outdir PATH]	
optional arguments: -h,help show this help message and exit associations-file ASSOC, -a ASSOC TFPG SMV associations file	
smv-file SMV, -m SMV	
SMV Model output-tfpg OUT_TFPG_FNAME, -o OUT_TFPG_FNAME Name of the synthesized TFPG	
engine {bmc,bdd,msat_bmc,sbmc,ic3,bmc_bdd,sbmc_bdd}, -E {bmc,bdd,msat_bmc,sbmc,ic3,bmc_bdd,sbmc_bdd} Use given engine (default: bmc)	
force-boolean-ic3, -B Force use of Boolean version of IC3 engine	
bmc-length BMCLEN,k BMCLEN	
Maximum path length for BMC	
dynamic, -D Enables dynamic reordering of variables	
boolean-conversion-uses-predicate-normalization, -b Enables predicate normalization during boolean conversion	
disable-coi-reduction, -c	
Disables TFPG graph simplification based on cone-of-influence	
prune-unreachable-nodes Enables a preprocessing step that removes unreachable	
nodes before synthesis is started.	
verbosity-level VERBOSELEVEL, -v VERBOSELEVEL	
Sets the output verbosity level outdir PATH, -O PATH	
Output directory, where all generated file should be put into (default: out)	

If the required options are not specified, error messages are shown to the user. Option -v can be set to enable verbose messages to the user.

C.7.4 TFPG Behavioral Validation

TFPG behavioral validation is carried out specifying a SMV model extended with fault behaviours, an associations file and a TFPG. The result is printed on standard output and a file containing the proof obligations is generated. If errors are encountered, logging files will be provided. The following script can be used for the task.

```
python ../../scripts/validate_tfpg_behavior.py -h
usage: validate_tfpg_behavior.py [-h] [--tfpg-file TFPG]
[--associations-file ASSOC] [--smv-file SMV]
[--boolean-conversion-uses-predicate-normalization]
[--dynamic-reordering]
[--delta-t-var DELTA_T_VAR]
[--bmc-length BMC_LEN]
[--property-to-validate {completeness,edge_tightness}]
[--monolithic-check] [--generate-only]
```

```
[--parametric-dump] [--outdir PATH]
optional arguments:
 --associations-file ASSOC, -a ASSOC

--smv-file SMV, -m SMV
                                SMV Model
   -boolean-conversion-uses-predicate-normalization, -b
Enables predicate normalization during boolean
                                 conversion
    -dynamic-reordering,
                                 -D
                                 Enables dynamic reordering of BDD variables
  delta t variable name

--bmc-length BMCLEN, -k BMCLEN

Maximum path length for BMC

--engine {msat_bmc, ic3}, -E {msat_bmc, ic3}

Use given engine (default: msat_bmc)

--property-to-validate {completeness, edge_tightness}, -p {completeness, edge_tightness}

Validate given property (default: completeness)

--monolithic-check. -M
    -monolithic-check, -M
                                 Check completeness proof obligations using a single
                                monolithic check
Only generate files for model checking
     generate-only, -g
  --parametric-dump, -P
                                 Dump parameterized completeness property
  --outdir PATH, -O PATH
                                 Output directory, where all generated file should be put into (default: out)
```

If the required options are not specified, error messages are shown to the user.

C.7.5 TFPG Tightening

TFPG tightening is carried out specifying a SMV model extended with fault behaviours, an associations file and a TFPG. The result is printed on standard output and a file containing the tightened TFPG is generated. If errors are encountered, logging files will be provided. The following script can be used for the task.

```
python .. / .. / scripts / tighten_tfpg.py -h
                                                -h
-file TFPG] [--ass
-tags TAGS]
                                --h] [--tfpg-file TFPG] [--associations-file A
--smv-file SMV] [--tags TAGS]
--delta-t-var DELTA.T.VAR]
--engine {conc,ia}] [--tmax-bound TMAX.BOUND]
--bmc-length BMC.LEN] [--ic3-length IC3.LEN]
--outdir PATH]
usage: tighten_tfpg.py
                                                                      ssociations-file ASSOC]
optional arguments:
    h, --help show this help message and exit -tfpg-file TFPG, -t TFPG
  --tipg-file fiFrG tile (can be in XML or human readable format)

--associations-file ASSOC, -a ASSOC

TFPG SMV associations file
   --smv-file SMV, -m SMV
SMV Model
  -- tags TAGS, -T TAGS Parameters to be tightened (tmin/tmax/modes; semi-
  --tmax-bound TMAX_BOUND
                                Upper bound for tmax (for tightening of
  --bmc-length BMC_LEN, -k BMC_LEN
  Maximum path length for BMC
--ic3-length IC3_LEN, -K IC3_LEN
                                Maximum number of frames for IC3
  --outdir PATH, -O PATH
                                Output directory, where all generated file should be put into (default: out)
```

If the required options are not specified, error messages are shown to the user.

C.7.6 TFPG Effectiveness Validation

TFPG effectiveness validation is carried out specifying a SMV model extended with fault behaviours and a target failure modes set. If errors are encountered, logging files will be provided. The following script is available for this task.

python .../../scripts/validate_tfpg_effectiveness.py -h-h] [--t --target $usage: \ validate_tfpg_effectiveness.py$ [--tfpg-file TFPG -fm-set FM_SET --sampling-rate SAMPLING_RATE] --target-system-mode TARGET_SYSTEM_MODE] [--bmc-length BMC_LENGTH] --use-bmc [--outdir PATH] optional arguments: show this help message and exit -h, --help show --tfpg-file TFPG, -t TFPG The TFPG file (can be in XML or human readable format) --target-fm-set FM_SET The set of FMs that need to be diagnosed (as a group colon-separated --sampling-rate SAMPLING_RATE The interval at which the monitored system is monitored -target-system-mode TARGET_SYSTEM_MODE The system mode **for** which effectiveness should be analyzed -use-bmc Use bounded model checking --bmc-length BMC_LENGTH Maximum path length for BMC --outdir PATH, -O PATH Output directory, where all generated file should be put into (default: out)

If the required options are not specified, error messages are shown to the user.

C.7.7 TFPG Statistics Information Extraction

Statistic information of a TFPG can be extracted specifying a valid TFPG file. The following script is available for this task.

```
python ../../scripts/stats_tfpg.py -h
usage: stats_tfpg.py [-h] [--syntax-only] inputTFPG
Statistics information on TFPG
positional arguments:
    inputTFPG The TFPG model to use
optional arguments:
    -h, --help show this help message and exit
    --syntax-only, -s Perform only syntactical analysis
```

If the required options are not specified, error messages are shown to the user.

C.7.8 TFPG Properties Check

Check for possibility, necessity, consistency and activability of a scenario is carried out specifying the desired scenario and a TFPG. The following script is available for this task.

If the required options are not specified, error messages are shown to the user.

C.7.9 TFPG Scenario Diagnosis

Diagnosis of a TFPG (and of a scenario) is carried out by specifying the TFPG (and the desired scenario). The following script is available for this task.

```
python ../../scripts/compute_tfpg_diagnosis.py -h
                                         [-h] [--diagnosability] [--diagnose scenario]
[--all-diag node] [--open-infinity]
usage: compute_tfpg_diagnosis.py
                                         inputTFPG
TFPG Diagnosis (SMT): Diagnose the scenario for the given TFPG.
positional arguments:
  inputTFPG
                             The TFPG model to use
optional arguments:
                             show this help message and exit
Checks if all failure modes are diagnosable
  -h, --help
    diagnosability
  _
  --diagnose scenario , -d scenario
                             Enumerate the possible diagnoses for the given scenario
  --all-diag node, -a node
                             Checks if the given node appears in all the diagnoses
Force the usage of Open Infinity semantics
  --open-infinity
```

If the required options are not specified, error messages are shown to the user.

C.7.10 TFPG Refinement Check

TFPG refinement check is carried out by specifying the original TFPG, the refined one and a mapping file. The following script is available for this task.

If the required options are not specified, error messages are shown to the user.

C.7.11 TFPG Filtering

TFPG filtering is carried out on a given TFPG file, using the following script:

C.8 Viewers

C.8.1 Trace viewer

The trace viewer can be used to display a trace generated by NUXMV or xSAP. It takes a trace file (XML format) and shows it. It allows also to specify filters which can be used to limit the symbols spaces and/or the steps which are show.

To run the Trace viewer, invoke script:

```
$> python .../../scripts/view_trace.py <trace-file.xml>
```

C.8.2 Fault Tree viewer

The fault tree viewer can be used to display a fault tree generated by xSAP. It takes a FT in FT-Plus format (two files for events and gates) or in XML format and shows it.

To run the Fault Tree viewer, invoke script:

Given a pair of events and gates files, (e.g. event.txt and gates.txt) it can show the fault tree with:

\$> python scripts/view_ft.py -e events.txt -g gates.txt

The same can be obtained giving an xml representation of it (e.g. ft.xml):

 $\gg python scripts / view_ft.py -x ft.xml$

However, it can be handy to exploit option -d (or its default to ./out) to automatically find fault tree generated by scripts/compute_ft.py. In this case, events and gates files are automatically selected only if there is no possible ambiguity, and choices are always reported by the verbose messages.

```
$> pwd
.../examples/fe/triple_modular_generator/
$> ls
out SC.TMG.smv SC.TMG.fei
$> python ../../.scripts/view_ft.py -v
INFO:Searching for suitable gates file
INFO:Found gates file 'out/extended_SC_TMGgates.txt'
INFO:Searching for suitable events file
INFO:Found events file 'out/extended_SC_TMGevents.txt'
```

This works only if scripts/compute_ft.py was called before invoking the ft viewer.

C.8.3 FMEA Table viewer

There is no viewer for FMEA at the moment. However, since FMEA is generated by xSAP as CSV file, text editors or spreadsheet programs like excel can be used to show it.

C.8.4 TFPG viewer

The TFPG viewer can be used to display a TFPG. It takes a TFPG in xml or textual format and shows it. The shown TFPG can also be moved, zoomed, edited and saved.

To run the TFPG viewer, invoke script:

It is possible to run the viewer in three ways:

- 1. with a TFPG file as argument; in this case, the selected TFPG is shown.
- 2. with a TFPG file and and output file as arguments; in this case, the corresponding TFPG is directly dumped to file
- 3. without parameters; in this case, an empty window is shown and it is possible to load the TFPG using the GUI.

Appendix D

Command Guide

This section provides a reference for the commands provided by xSAP.

We remark that xSAP also includes the commands of the NUXMV model checker. Please refer to the NUXMV documentation, available from the NUXMV home page (http://nuxmv.fbk.eu/)

D.1 Invoking xSAP

xSAP can be invoked from the command line in the following way:

\$> <path_to_xsap>/xsap -int -sa_compass -sa_compass_task out/fms_SC_TMG.xml
 out/extended_SC_TMG.smv

where

- out/fms_SC_TMG.xml is the fault modes xml file;
- out/extended_SC_TMG.smv is the extended smv model.

The command-line options can be changed using environmental variables. This also makes it possible to change the name of the fault modes xml file within an xSAP session. For instance, the following is an example xSAP session.

```
$> <path_to_xsap>/xsap -int
xSAP > set input_file out/extended_SC_TMG.smv
xSAP > set sa_compass
xSAP > set sa_compass_task_file out/fms_SC_TMG.xml
```

D.2 Properties vs TLEs

Several commands accept as input properties and/or TLEs.

Properties describe a good behaviour of the system ("the airplane can always fly"), while a TLE represents a bad state, the TLE leading to a failure.

Properties are taken from the properties database and are read from the input SMV file or added at runtime with command add_property. When stored in the database, properties can be referred through their numeric ID (*index*) or optionally through their string name when available. When specifying a property in the database, both invariant and LTL specifications are allowed. Invariants accept the next operator, LTL specifications must be restricted to the fragment of LTL which can be translated to invariant with next. E.g. G p, (G p) -> (G q) are valid LTL properties for Safety Assessment. An example of an invalid LTL specification is G (p -> F q) which cannot be converted to an invariant.

Differently from properties, TLEs can be specified only as invariants specifications accepting the **next** operator.

The Safety Assessment commands transparently convert all legal LTL properties to an invariants, while negating them to obtain the corresponding TLEs, and base their analysis on the obtained TLEs.

D.3 Automated Fault extension

The **fe_extend_module** command performs automatic fault extension of the nominal model according to the previously loaded specification.

By using the information previously loaded with command fe_load_doc, the command performs the automatic fault extension of the currently loaded model and dumps the extended model to a new SMV file. Optionally, it creates also an XML file containing information about Fault Modes and Common Causes, with the possible associated information about fault probability.

Notice that at the moment the currently loaded FSM remains untouched, meaning that if you need to work on the extended model, you will have to reset and load it explicitly with read_model.

```
usage: fe_extend_module [-h] [-m fname] [-A] [-c] -o fname
-h Prints the command usage.
-m fname Dumps the fault mode predicates to the given file.
-o fname Dumps the extended HRC to the given file.
-A Anonymize the output.
-c Disable generation of Common Causes
```

The fe_load_doc command loads the fault extension specification XML file. Reads the given extension-file.xml containing the specification of the fault extension, and fills internal structures to prepare the extension. It performs syntactic and semantic checks, and report checking errors and warnings. The path to the directory containing file fe.dtd can be specified with option -p or with the environment variable XSAP_LIBRARY_PATH.

```
usage: fe_load_doc [-h] [-c] [-o fname] [-F xml|text] [-p path] -i fname
-h Prints this command usage
-i fname Loads the given XML extension file
-c Disables syntactic and semantic checks
-o fname Dumps errors and warnings to the specified output file
-F text|xml Format to be used when dumping errors and warnings [text]
-p path Sets the path where file 'fe.dtd' is located
```

D.4 Printing the Fault Variables

The show_fault_variables command lists the specified fault variables.

usage: show_fault_variables [-h] [-m| -o file] [-v]
-h Prints the command usage
-m Pipes output through the program specified by
 the "PAGER" shell variable if defined,
 else through UNIX "more"
-o file Writes the generated output to "file"
-v Prints verbosely

D.5 Computing Monotonic Fault Tree

The compute_fault_tree is the command to compute fault trees for monotonic systems, using the standard BDD-based engine.

```
usage: compute_fault_tree [-f] [-h] [-m| -o file] [-N NR_FAIL]
                          [-d] [-t [-Q]] [-e] [-p [-S]]
                          [-x "prefix_string"]
                          [-n index | -P name | "next-expr"]
   -h
               Prints the command usage
               Pipes output through the program specified by
   -m
               the "PAGER" shell variable if defined,
               else through UNIX "more"
  -o file
               Writes the generated output to "file"
  -N NR_FAIL Limit number of possible failures to NR_FAIL
  -d
               Generates a dynamic fault tree
   -t
               Generates fault tree
               Disables construction of ordered FT and computes probability
   -Q
               only for the top level event (to speedup computation)
   -е
               Prints counterexample traces
               Computes probability
   -p
   -S
               Computes probability also in symbolic form
               (python and octave/matlab)
  -x prefix
               Prefixes generated file names with "prefix"
               Use given INVARSPEC or LTLSPEC property as !TLE
   -n index
   -P name
               Use given INVARSPEC or LTLSPEC property (from name) as !TLE
  next-expr
              Use given expression as TLE
  Options -n, -P and "next-expr" are mutually exclusive.
  If none is specified, all INVARSPEC and LTLSPEC (which can be converted
```

into INVARSPEC) properties will be used to generate multiple fault trees. In this case, the index of each property will appear in the prefix of generated files.

Also notice that -n and -P take a property which is supposed to hold in the nominal model, i.e. that describes a good behaviour ("the airplane can always fly"). Instead the expression in "next-expr" represents a bad state, a failure TLE.

Example

xSAP > set input_file model.smv xSAP > go xSAP > compute_fault_tree -e -t "property-text"

The compute_fault_tree_bmc is the command to compute fault trees for monotonic systems, using the SAT-based engine.

usage: compute_fault_tree_bmc [-h] [-m| -o file] [-k length] [-l loopback] [-T mcs] [-N NR_FAIL] [-d] [-t [-Q]] [-e] [-p [-S]] [-x "prefix_string"] [-n index | -P name | "next-expr"] -h Prints the command usage Pipes output through the program specified by -m the "PAGER" shell variable if defined, else through UNIX "more" -o file Writes the generated output to "file" -k length Set problem length to length -l loop Set loopback value to loop -T mcs Limit number of cut sets to be computed to mcs -N NR_FAIL Limit number of possible failures to NR_FAIL -d Generates a dynamic fault tree -t Generates fault tree -Q Disables construction of ordered FT and computes probability only for the top level event (to speedup computation) Prints counterexample traces -e Computes probability -p Computes probability also in symbolic form -S (python and octave/matlab) -x prefix Prefixes generated file names with "prefix" -n index Use given INVARSPEC or LTLSPEC property as !TLE -P name Use given INVARSPEC or LTLSPEC property (from name) as !TLE next-expr Use given expression as TLE Options -n, -P and "next-expr" are mutually exclusive. If none is specified, all INVARSPEC and LTLSPEC (which can be converted into INVARSPEC) properties will be used to generate multiple fault trees. In this case, the index of each property will

appear in the prefix of generated files.

Also notice that -n and -P take a property which is supposed to hold in the nominal model, i.e. that describes a good behaviour ("the airplane can always fly"). Instead the expression in "next-expr" represents a bad state, a failure TLE.

Example

xSAP > set input_file model.smv xSAP > go_bmc xSAP > compute_fault_tree_bmc -e -t "property-text"

The compute_fault_tree_bmc_inc is the command to compute fault trees for monotonic systems, using the SAT-based engine and incremental verification. It requires an incremental SAT solver.

```
usage: compute_fault_tree_bmc_inc [-h] [-m| -o file] [-k length]
```

[-1 loopback] [-T mcs] [-N NR_FAIL] [-d] [-t [-Q]] [-e] [-p [-S]] [-x "prefix_string"] [-n index | -P name | "next-expr"] -h Prints the command usage Pipes output through the program specified by -m the "PAGER" shell variable if defined, else through UNIX "more" -o file Writes the generated output to "file" -k length Set problem length to length -l loop Set loopback value to loop -T mcs Limit number of cut sets to be computed to mcs -N NR_FAIL Limit number of possible failures to NR_FAIL Generates a dynamic fault tree -d Generates fault tree -t -Q Disables construction of ordered FT and computes probability only for the top level event (to speedup computation) Prints counterexample traces -e Computes probability -p Computes probability also in symbolic form -S (python and octave/matlab) -x prefix Prefixes generated file names with "prefix" -n index Use given INVARSPEC or LTLSPEC property as !TLE -P name Use given INVARSPEC or LTLSPEC property (from name) as !TLE next-expr Use given expression as TLE Options -n, -P and "next-expr" are mutually exclusive.

If none is specified, all INVARSPEC and LTLSPEC (which can be converted into INVARSPEC) properties will be used to generate multiple fault trees. In this case, the index of each property will appear in the prefix of generated files.

Also notice that -n and -P take a property which is supposed to hold in the nominal model, i.e. that describes a good behaviour ("the airplane can always fly"). Instead the expression in "next-expr" represents a bad state, a failure TLE.

Example

```
xSAP > set input_file model.smv
xSAP > go_bmc
xSAP > compute_fault_tree_bmc_inc -e -t "property-text"
```

The compute_fault_tree_sbmc_inc is the command to compute fault trees for monotonic systems, using the SAT-based engine, next bounded model checking and incremental verification. It requires an incremental SAT solver.

usage: compute_fault_tree_sbmc_inc [-h] [-m| -o file] [-k length] [-T mcs] [-N NR_FAIL] [-d] [-t [-Q]] [-e] [-p [-S]] [-V] [-B] [-c] [-x "prefix_string"] [-n index | -P name | "next-expr"] -h Prints the command usage Pipes output through the program specified by -m the "PAGER" shell variable if defined, else through UNIX "more" -o file Writes the generated output to "file" -k length Set problem length to length -T mcs Limit number of cut sets to be computed to mcs -N NR_FAIL Limit number of possible failures to NR_FAIL -d Generates a dynamic fault tree -t Generates fault tree -Q Disables construction of ordered FT and computes probability only for the top level event (to speedup computation) Prints counterexample traces -e Computes probability -p Computes probability also in symbolic form -S (python and octave/matlab) -V Does not perform virtual unrolling -B Adds blocking clauses at all time steps -c Performs completeness check -x prefix Prefixes generated file names with "prefix" Use given INVARSPEC or LTLSPEC property as !TLE -n index Use given INVARSPEC or LTLSPEC property (from name) as !TLE -P name next-expr Use given expression as TLE Options -n, -P and "next-expr" are mutually exclusive. If none is specified, all INVARSPEC and LTLSPEC (which can be converted into INVARSPEC) properties will be used to generate multiple fault trees. In this case, the index of each property will appear in the prefix of generated files. Also notice that -n and -P take a property which is supposed to hold in the nominal model, i.e. that describes a good behaviour ("the airplane can always fly"). Instead the expression in "next-expr" represents a bad state, a failure TLE. Example xSAP > set input_file model.smv xSAP > go_bmc

xSAP > compute_fault_tree_sbmc_inc -e -t "property-text"

The compute_fault_tree_bmc_bdd is the command to compute fault trees for monotonic systems, using both the BDD-based and the SAT-based engine.

usage: compute_fault_tree_bmc_bdd [-h] [-m| -o file] [-k length] [-l loopback] [-T mcs] [-N NR_FAIL] [-d] [-t [-Q]] [-e] [-p [-S]] [-x "prefix_string"] [-n index | -P name | "next-expr"] -h Prints the command usage Pipes output through the program specified by -m the "PAGER" shell variable if defined, else through UNIX "more" -o file Writes the generated output to "file" -k length Set problem length to length -l loop Set loopback value to loop -T mcs Limit number of cut sets to be computed to mcs -N NR_FAIL Limit number of possible failures to NR_FAIL -d Generates a dynamic fault tree -t Generates fault tree -Q Disables construction of ordered FT and computes probability only for the top level event (to speedup computation) Prints counterexample traces -e -p Computes probability -S Computes probability also in symbolic form (python and octave/matlab) -x prefix Prefixes generated file names with "prefix" Use given INVARSPEC or LTLSPEC property as !TLE -n index -P name Use given INVARSPEC or LTLSPEC property (from name) as !TLE next-expr Use given expression as TLE Options -n, -P and "next-expr" are mutually exclusive. If none is specified, all INVARSPEC and LTLSPEC (which can be converted into INVARSPEC) properties will be used to generate multiple fault trees. In this case, the index of each property will appear in the prefix of generated files. Also notice that -n and -P take a property which is supposed to hold in the nominal model, i.e. that describes a good behaviour ("the airplane can always fly"). Instead the expression in "next-expr" represents a bad state, a failure TLE. The compute_fault_tree_bmc_inc_bdd is the command to compute fault trees for monotonic systems, using both the BDD-based and the SAT-based engine. It requires an incremen-

```
[-d] [-t [-Q]] [-e]
[-p [-S]] [-x "prefix_string"]
```

	[-n index -P name "next-expr"]		
-h	Prints the command usage		
-m	Pipes output through the program specified by		
	the "PAGER" shell variable if defined,		
	else through UNIX "more"		
-o file	Writes the generated output to "file"		
-k length	Set problem length to length		
-l loop	Set loopback value to loop		
-T mcs	Limit number of cut sets to be computed to mcs		
-N NR_FAIL	Limit number of possible failures to NR_FAIL		
-d	Generates a dynamic fault tree		
-t	Generates fault tree		
-Q	Disables construction of ordered FT and computes probability		
	only for the top level event (to speedup computation)		
-е	Prints counterexample traces		
-р	Computes probability		
-S	Computes probability also in symbolic		
	form (python and octave/matlab)		
-x prefix	Prefixes generated file names with "prefix"		
-n index	Use given INVARSPEC or LTLSPEC property as !TLE		
-P name	Use given INVARSPEC or LTLSPEC property (from name) as !TLE		
next-expr	Use given expression as TLE		

Options -n, -P and "next-expr" are mutually exclusive. If none is specified, all INVARSPEC and LTLSPEC (which can be converted into INVARSPEC) properties will be used to generate multiple fault trees. In this case, the index of each property will appear in the prefix of generated files.

Also notice that -n and -P take a property which is supposed to hold in the nominal model, i.e. that describes a good behaviour ("the airplane can always fly"). Instead the expression in "next-expr" represents a bad state, a failure TLE.

Example

xSAP > set input_file model.smv xSAP > go xSAP > go_bmc xSAP > compute_fault_tree_bmc_inc_bdd -e -t "property-text"

The compute_fault_tree_sbmc_inc_bdd is the command to compute fault trees for monotonic systems, using both the BDD-based and the SAT-based engine, simple bounded model checking and incremental verification. It requires an incremental SAT solver.

[-e] [-p [-S]] [-V] [-B] [-c] [-x "prefix_string"] [-n index | -P name | "next-expr"] -h Prints the command usage Pipes output through the program specified by -m the "PAGER" shell variable if defined, else through UNIX "more" -o file Writes the generated output to "file" -k length Set problem length to length -T mcs Limit number of cut sets to be computed to mcs -N NR_FAIL Limit number of possible failures to NR_FAIL -d Generates a dynamic fault tree Generates fault tree -t -Q Disables construction of ordered FT and computes probability only for the top level event (to speedup computation) Prints counterexample traces -0 Computes probability -p Computes probability also in symbolic form -S (python and octave/matlab) -V Does not perform virtual unrolling -B Adds blocking clauses at all time steps -c Performs completeness check -x prefix Prefixes generated file names with "prefix" -n index Use given INVARSPEC or LTLSPEC property as !TLE Use given INVARSPEC or LTLSPEC property (from name) as !TLE -P name next-expr Use given expression as TLE Options -n, -P and "next-expr" are mutually exclusive. If none is specified, all INVARSPEC and LTLSPEC (which can be converted

If none is specified, all INVARSPEC and LTLSPEC (which can be converted into INVARSPEC) properties will be used to generate multiple fault trees. In this case, the index of each property will appear in the prefix of generated files.

Also notice that -n and -P take a property which is supposed to hold in the nominal model, i.e. that describes a good behaviour ("the airplane can always fly"). Instead the expression in "next-expr" represents a bad state, a failure TLE.

Example

```
xSAP > set input_file model.smv
xSAP > go
xSAP > go_bmc
xSAP > compute_fault_tree_sbmc_inc_bdd -e -t "property-text"
```

The compute_fault_tree_msat_bmc is the command to compute fault trees for monotonic systems, using the SMT-based engine. It requires an SMT solver.

usage: compute_fault_tree_msat_bmc [-h] [-m | -o file] [-k length] [-1 loopback] [-T mcs] [-N NR_FAIL] [-t [-Q]] [-e] [-p [-S]] [-M] [-x "prefix_string"] [-n index | -P name | "next-expr"] -h Prints the command usage Pipes output through the program specified by -m the "PAGER" shell variable if defined, else through UNIX "more" -o file Writes the generated output to "file" -k length Set problem length to length -l loop Set loopback value to loop -T mcs Limit number of cut sets to be computed to mcs -N NR_FAIL Limit number of possible failures to NR_FAIL -t Generates fault tree -Q Disables construction of ordered FT and computes probability only for the top level event (to speedup computation) -M Set the generation of monotonic cutsets to FALSE -e Prints counterexample traces Computes probability -p -S Computes probability also in symbolic form (python and octave/matlab) -x prefix Prefixes generated file names with "prefix" -n index Use given INVARSPEC or LTLSPEC property as !TLE -P name Use given INVARSPEC or LTLSPEC property (from name) as !TLE next-expr Use given expression as TLE Options -n, -P and "next-expr" are mutually exclusive. If none is specified, all INVARSPEC and LTLSPEC (which can be converted into INVARSPEC) properties will be used to generate multiple fault trees. In this case, the index of each property will appear in the prefix of generated files.

Also notice that -n and -P take a property which is supposed to hold in the nominal model, i.e. that describes a good behaviour ("the airplane can always fly"). Instead the expression in "next-expr" represents a bad state, a failure TLE.

Example

xSAP > set input_file model.smv xSAP > go_msat xSAP > compute_fault_tree_msat_bmc -e -t "property-text"

The compute_fault_tree_param is the command to compute fault trees for monotonic systems, using an engine based on IC3 and parameter synthesis, with a SAT or SMT backend.

usage: compute_fault_tree_param [-h] [-m| -o file] [-t [-Q] [-I]] [-e] [-p [-S]] [-N NR_FAIL] [-x "prefix_string"] [-i] [-L] [-k depth] [-n index | -P name | "next-expr"] -h Prints the command usage Pipes output through the program specified by -m the "PAGER" shell variable if defined, else through UNIX "more" -o file Writes the generated output to "file" -t Generates fault tree -Q Disables construction of ordered FT and computes probability only for the top level event (to speedup computation) -e Prints counterexample traces Computes probability -p -S Computes probability also in symbolic form (python and octave/matlab) -N NR_FAIL Limit number of possible failures to NR_FAIL Prefixes generated file names with "prefix" -x prefix -i Forces the use of SMT for finite models -L Disable layering -k depth Use an initial BMC run up to the given depth Generates intermediate fault trees for each layer -T -n index Use given invar property as !TLE -P name Use given invar property (from name) as !TLE Use given expression as TLE next-expr

Options -n, -P and "next-expr" are mutually exclusive. If none is specified, all INVARSPEC and LTLSPEC (which can be converted into INVARSPEC) properties will be used to generate multiple fault trees. In this case, the index of each property will appear in the prefix of generated files.

Also notice that -n and -P take a property which is supposed to hold in the nominal model, i.e. that describes a good behaviour ("the airplane can fly"). Instead the expression in "next-expr" represents a bad state, a failure TLE.

Example

xSAP > set input_file model.smv xSAP > go_bmc xSAP > compute_fault_tree_param -e -t "property-text"

The compute_fault_tree_param_klive is the command to compute fault trees for monotonic systems, using the SAT-based engine and an invariant checking termination procedure based on IC3. usage: compute_fault_tree_param_klive [-h] [-m| -o file] [-t [-Q] [-I]] [-L] [-A] [-e] [-p [-S]] [-N NR_FAIL] [-x "prefix_string"] [-i] [-n index | -P name | "ltl-expr"] -h Prints the command usage Pipes output through the program specified by -m the "PAGER" shell variable if defined, else through UNIX "more" -o file Writes the generated output to "file" -t Generates fault tree -Q Disables construction of ordered FT and computes probability only for the top level event (to speedup computation) -L Disable layering -A Force the computing of upper and lower probability bounds -e Prints counterexample traces Computes probability -p -S Computes probability also in symbolic form (python and octave/matlab) -N NR_FAIL -x prefix Prefixes generated file names with "prefix" -i Forces the use of SMT for finite models -I Generates intermediate fault trees for each layer -n index Use given ltl property as !TLE -P name Use given 1tl property (from name) as !TLE ltl-expr Use given expression as TLE Options -n, -P and "ltl-expr" are mutually exclusive.

Also notice that -n and -P take a property which is supposed to hold in the nominal model, i.e. that describes a good behaviour ("the airplane can always fly"). Instead the expression in "ltl-expr" represents a bad condition.

D.6 Computing Non Monotonic Fault Tree

The compute_prime_implicants computes prime implicants and generates a fault tree for a top level event given as a simple expression; non-failure variables are existentially quantified.

```
usage: compute_prime_implicants [-h] [-m| -o file] [-t] [-e] [-p [-S]]
                                [-x "prefix_string"]
                                [-n index | -P name | "next-expr"]
   -h
             Prints the command usage
             Pipes output through the program specified by
   -m
             the "PAGER" shell variable if defined,
             else through UNIX "more"
  -o file
             Writes the generated output to "file"
   -t
             Generates fault tree
             Prints counterexample traces
   -e
             Computes probability
   -p
   -S
             Computes probability also in symbolic
             form (python and octave/matlab)
   -x prefix Prefixes generated file names with "prefix"
  -n index Use given LTL property as !TLE
  -P name
             Use given LTL property (from name) as !TLE
  next-expr Use given expression as TLE
  Options -n, -P and "next-expr" are mutually exclusive.
  If none is specified, all INVAR properties will be used to generate
  multiple fault trees. In this case, the index of each property will
   appear in the prefix of generated files.
  Also notice that -n and -P take a property which is supposed to
  hold in the nominal model, i.e. that describes a good behaviour
   ("the airplane can always fly"). Instead the expression in "next-expr"
```

Example

```
xSAP > set input_file model.smv
xSAP > go
xSAP > compute_prime_implicants -e -t "property-text"
```

represents a bad state, a failure TLE.

The compute_prime_implicants_param computes prime implicants and generates a fault tree for a top level event given as a simple expression, using parameter synthesis.

the "PAGER" shell variable if defined, else through UNIX "more" -o file Writes the generated output to "file" -t Generates fault tree -Q Disables construction of ordered FT and computes probability only for the top level event (to speedup computation) -e Prints counterexample traces -p Computes probability -S Computes probability also in symbolic form (python and octave/matlab) -x prefix Prefixes generated file names with "prefix" -i Forces the use of SMT for finite models -L Disable layering -n index Use given invar property as !TLE -P name Use given invar property (from name) as !TLE next-expr Use given expression as TLE

Options -n, -P and "simple-expr" are mutually exclusive. If none is specified, all invar properties will be used to generate multiple fault trees. In this case, the index of each property will appear in the prefix of generated files.

Also notice that -n and -P take a property which is supposed to hold in the nominal model, i.e. that describes a good behaviour ("the airplane can fly"). Instead the expression in "simple-expr" represents a bad state, a failure TLE.

Example

xSAP > set input_file model.smv xSAP > go_bmc xSAP > compute_prime_implicants_param -e -t "property-text"

D.7 Computing FMEA Table

The compute_fmea_table command is the command to compute fmea tables, using the standard BDD-based engine.

usage: compute_fmea_table [-h] [-m| -o file] [-c] [-d] [-N NR_FAIL] [-t] [-e] [-x "prefix_string"] [-n <props> | -P <props> | "next-expr" ... "next-expr"] -h Prints the command usage -m Pipes output through the program specified by the "PAGER" shell variable if defined, else through UNIX "more" -c Generates a compact FMEA table -d Generates a dynamic FMEA table -N NR_FAIL set number of failures to NR_FAIL (default: 1) -o file Writes the generated output to "file" -t Generates fmea table -e Prints counterexample traces -x prefix Prefixes generated file names with "prefix" -n "props" A subset of INVARSPEC or LTLSPEC properties whose indices are given as argument. Indices are separated by comma ',' or colon ':'. Ranges are allowed where lower and upper bounds are separated by dash '-'. For example "1:3-6:8" for indices 1,3,4,5,6,8 -P "props" A subset of INVARSPEC or LTLSPEC properties whose names are given as argument. Names are separated by comma ',' or colon ':'.

Options -n, -P and "next-expr" ... "next-expr" are mutually exclusive. If none is specified, all INVARSPEC and LTLSPEC (which can be converted into INVARSPEC) properties will be used.

Also notice that -n and -P take properties which are supposed to hold in the nominal model, i.e. that describe a good behaviour ("the airplane can always fly"). Instead the expressions in "next-expr" represent a bad state, a failure TLE.

Example

```
xSAP > set input_file model.smv
xSAP > go
xSAP > compute_fmea_table -t "property-text"
```

The compute_fmea_table_bmc_inc command is the command to compute FMEA tables, using the SAT-based engine and incremental verification. It requires an incremental SAT solver.

```
-h Prints the command usage
 -m Pipes output through the program specified by
    the "PAGER" shell variable if defined,
      else through UNIX "more"
-k length set problem length to length
-l loop set loopback value to loop
 -c Generates a compact FMEA table
-N NR_FAIL set number of failures to NR_FAIL (default: 1)
-o file Writes the generated output to "file"
-t Generates fmea table
-e Prints counterexample traces
-x prefix Prefixes generated file names with "prefix"
-n "props" A subset of INVARSPEC or LTLSPEC properties whose indices are given
as argument. Indices are separated by comma ',' or colon ':'.
Ranges are allowed where lower and upper bounds are separated
by dash '-'. For example "1:3-6:8" for indices 1,3,4,5,6,8
-P "props" A subset of INVARSPEC or LTLSPEC properties whose names are given
as argument. Names are separated by comma ',' or colon ':'.
```

Options -n, -P and "next-expr" ... "next-expr" are mutually exclusive. If none is specified, all INVARSPEC and LTLSPEC (which can be converted into INVARSPEC) properties will be used.

Also notice that -n and -P take properties which are supposed to hold in the nominal model, i.e. that describe a good behaviour ("the airplane can always fly"). Instead the expressions in "next-expr" represent a bad state, a failure TLE.

Example

xSAP > set input_file model.smv xSAP > go_bmc xSAP > compute_fmea_table_bmc_inc -t "property-text"

The compute_fmea_table_msat_bmc command is the command to compute FMEA tables, using the SMT-based engine. It requires an SMT solver.

-N NR_FAIL set number of failures to NR_FAIL (default: 1) -o file Writes the generated output to "file" -t Generates fmea table -e Prints counterexample traces -x prefix Prefixes generated file names with "prefix" -n "props" A subset of INVARSPEC or LTLSPEC properties whose indices are given as argument. Indices are separated by comma ',' or colon ':'. Ranges are allowed where lower and upper bounds are separated by dash '-'. For example "1:3-6:8" for indices 1,3,4,5,6,8 -P "props" A subset of INVARSPEC or LTLSPEC properties whose names are given as argument. Names are separated by comma ',' or colon ':'.

Also notice that -n and -P take properties which are supposed to hold in the nominal model, i.e. that describe a good behaviour ("the airplane can always fly"). Instead the expressions in "next-expr" represent a bad state, a failure TLE.

Example

xSAP > set input_file model.smv xSAP > go_msat xSAP > compute_fmea_table_msat_bmc -t "property-text"

into INVARSPEC) properties will be used.

D.8 Computing MTCS

```
usage: compute_mode_transition_cut_sets [-h] [-v] [-e] [-M] [-s] [-i] [-L]
                                        [-t] [-o format] [-g] [-x prefix]
                                       expr_1 ... expr_n
   -h Prints the command usage
   -v Use provided expressions as variables. The states are computed
      as all evaluations of the variables
   -e Compute only transitions from the first mode to the others
   -M Turn off monotonicity
   -s Use strict MTCS computation
   -i Forces the use of SMT for finite models
   -L Disable layering
   -t Print XML output in file
   -o format Print visual output in file (dot, tex)
   -g Paging, print each mode transition in separate file
   -x prefix Prefixes generated file names with "prefix"
  Example
xSAP > set input_file model.smv
xSAP > go_bmc
xSAP > compute_mode_transition_cut_sets -t 'mode-expression'
```

```
'mode-expression'
```

'mode-expression'

D.9 Checking diagnosability

The build_twin_plant command is used to build the twin plant of a given model. Note that option "-p" is always required to be set, and kept only for backward-compatibility reasons.

```
usage: build_twin_plant -o <str> [-a] [-f] [-A] [-p] [-t <str>] [-h]
-o file containing list of observable variables
[-a] asynchronous twinplant composition [option is deprecated] (default: false)
[-f] configure twin plant for synthesis [option is deprecated] (c1/c2 specification)
[-A] configure twin plant for synthesis (ASL specification)
[-p] use history variables to encode twin plant
[-t] name of delta_t variable [option is deprecated]
[-h] prints command usage
```

Example

```
xSAP > set input_file examples/FDI/extended_SC_TMG_empty_controller.smv
xSAP > build_twin_plant -o examples/FDI/diag/G1_observables.obs -p
```

The diag_load_asl_spec command is used to load a diagnosis condition for diagnosability analysis.

```
usage: diag_load_asl_spec [-f <str>|-a <str> -c <str> -d <int> [-x <str>]] [-p] [-h]
    -f file containing the list of ASL specifications
    -a ASL alarm type (exact/bounded/finite)
    -c diagnosis condition (LTL expression)
    -d delay bound (for exact and bounded delay)
    [-x] context (LTL expression)
    [-p] remove existing properties from property manager
    [-h] prints command usage
```

Example

```
xSAP > set input_file examples/FDI/extended_SC_TMG_empty_controller.smv
xSAP > build_twin_plant -o examples/FDI/diag/G1_observables.obs
xSAP > diag_load_asl_spec -p -a finite -c "(CN.cmd_G1 = cmd_on &
    (SC.G1.Gen_StuckOff.mode != NOMINAL |
    SC.G1.Gen_StuckOff.event = stuckAt_Off#failure))"
```

Any available model-checking command can then be used to verify the generated diagnosability proof obligations. Multiple specifications can be loaded from a file using the option -f.

```
xSAP > set input_file examples/FDI/extended_SC_TMG_empty_controller.smv
xSAP > build_twin_plant -o examples/FDI/diag/G1_observables.obs -p
xSAP > diag_load_asl_spec -p -f examples/FDI/diag/G1.asl
```

D.10 Minimum observables set analysis

The diag_optimize_observables_asl command is used to synthesize a sets of observables guaranteeing diagnosability.

Similarly as for diag_load_asl_spec it is possible to specify either a single alarm specification, or multiple alarm specifications through the use of an ASL specification file. If a specification is given, the result is a sensor configuration that satisfies all alarm conditions at the same time.

```
xSAP > set input_file examples/FDI/extended_SC_TMG_empty_controller.smv
xSAP > build_twin_plant -o examples/FDI/diag/G1_observables.obs -p -A
xSAP > go_bmc
xSAP > diag_optimize_observables_asl -f examples/FDI/diag/G1.asl
```

D.11 Synthesizing FD components

The synth_asynchronous_composition_semantics environment variable sets the semantics of the composition between the FD and the system. If set to 0, it enforces the synchronous semantics; if set to 1, it enforces the asynchronous semantics. For SMV models, synchronous semantics should be used. See [10] for additional information.

Example

```
xSAP > set input_file examples/FDI/extended_SC_TMG_empty_controller.smv
xSAP > set synth_asynchronous_composition_semantics 1
xSAP > go
```

The synth_FD command is used to generate the FD component of a given model.

```
Performs the FD Synthesis taking into account the
observables list and the alarm specifications.
usage: synth_FD [-h] -o <file> -f <file>
                 file containing list of observable variables
         -0
         -f
                 file containing the ASL specification
         -h
                 prints the command usage.
  Example
xSAP> set input_file examples/FDI/extended_SC_TMG_empty_controller.smv
xSAP> set synth_asynchronous_composition_semantics 0
xSAP> go
xSAP> synth_FD -o extended_SC_TMG.obs
               -f extended_SC_TMG.asl
   With extended_SC_TMC.obs:
CN.cmd_G1
SC.G1.state
   With extended_SC_TMC.asl:
NAME: FAULT
CONDITION: CN.cmd_G1 = cmd_on & (SC.G1.Gen_StuckOff.mode != NOMINAL &
           SC.G1.Gen_StuckOff.event = stuckAt_Off#failure)
TYPE: bounded
CONTEXT: GF (CN.cmd_G2 = cmd_on)
DELAY: 5
```

The synth_dump_fdir command is used to dump (to file) the generate model containing also the FD component.

usage:	<pre>synth_dump_fdir</pre>	[-o <file>] [-c] [-s]</file>
	-o <file></file>	The file on which to print
	-c	Dumps the FDIR combined within the original FSM
	-s	Prints some additional statistics
	-v	Be verbose in the transition relation printing

Example